B.Sc.ELECTRONICS

Syllabus

AFFILIATED COLLEGES

Program Code: 22M

2025-2026



BHARATHIAR UNIVERSITY

A State University, Accredited with "A" Grade by NAAC, Ranked 13th among Indian Universities by MHRD-NIRF,

Program Educational Objectives (PEOs)								
The B. Sc. Electronics program describe accomplishments that graduates are expected to attain within five to seven years after graduation								
PEO1	Provide graduates with a strong foundation in Electronics domain and to enable them to devise and deliver efficient solutions to challenging problems in Electronics, Communications and allied disciplines.							
PEO2	Impart analytic and thinking skills to develop initiatives and innovative ideas for R&D, Industry and societal requirements.							
PEO3	Provide sound theoretical and practical knowledge of Electronics, managerial and entrepreneurial skills to enable students to contribute to the wellbeing of society with a global outlook.							
PEO4	Inculcate qualities of teamwork as well as social, interpersonal and leadership skills and an ability to adapt to evolving professional environments in the domains of engineering and technology.							
PEO5	Motivate graduates to become good human beings and responsible citizens for the overall welfare of the society.							
PEO6	Develop attitude in lifelong learning, applying and adapting new ideas and technologies as their field evolves.							
PEO7	To prepare graduates who will have knowledge, ability and courage to pursue higher studies and research.							

Program Specific Outcomes (PSOs)								
After the suc	After the successful completion of B.Sc. Electronics program, the students are expected to							
PSO1	PSO1 Demonstrate proficiency in use of software and hardware required to practice electronics and communication profession.							
PSO2	Graduates will be able to apply fundamentals of electronics in various domains of analog and digital systems							
PSO3	Apprehend and Analyzes specific engineering problems of communication, electronic circuits, computer programming, embedded systems, VLSI design and semiconductor technology by applying the knowledge of basic sciences, engineering mathematics and engineering fundamentals.							
PSO4	Ability to communicate effectively with excellent interpersonal skills and demonstrate the practice of professional ethics for societal benefit							
PSO5	Graduates will be able to apply fundamentals of electronics in various domains of analog and digital systems.							
PSO6	Use embedded system concepts for developing IoT applications							



Program Outcomes (POs)								
On successfu	On successful completion of the B. Sc. Electronics program							
PO1	Engineering knowledge : Apply the knowledge of mathematics, Science, Engineering fundamentals and an engineering specialization to the solution of complex engineering problems							
PO2	Problem analysis: Identify, formulate, review research literature and Analyzes complex engineering problems reaching substantiated conclusion using principles of mathematics and Engineering sciences							
PO3	Design/Development of solutions: Design solutions for complex Engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal and environmental conditions.							
PO4	Conduct investigation of complex problems: Use research based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.							
PO5	Modern tool usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations							
PO6	The engineer and society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.							
PO7	Environment and Sustainability: Understand the impact of the professional engineering solution in societal and environmental contexts, and demonstrate the knowledge of and need fir sustainable development							
PO8	Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.							
PO9	Individual and team work: Function effectively as an individual, an as a member or leader in diverse teams, and in multidisciplinary settings.							
PO10	Life-Long learning : Recognize the need for and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.							

BHARATHIAR UNIVERSITY: COIMBATORE 641 046

B. Sc. Electronics Curriculum (University Affiliated Colleges)

Scheme of Examination (CBCS PATTERN)

(For the students admitted during the academic year 2025 – 26 onwards)

Course	Title of the Course	Credits	Н	ours	Maximum Marks			
Code			Theory	Practical	CIA	ESE	TOTAL	
		FIRST SE	MESTER	<u> </u>				
11T	Language –I	4	6	-	25	75	100	
12E	English – I	4	4	- 1	25	75	100	
13A	Core Paper I : Basic Electronics	4	6	20	25	75	100	
	Core Practical I: Basic Electronics Lab			3		-	-	
	Core Practical II : Semiconductor Devices Lab			3	क्राप	1	_	
1AA	Allied I: Mathematics—I	4	6	2.1	25	75	100	
1FA	Environmental Studies *	2	2		-	50	50	
	Total	18	T 24	L 06	100	350	450	
		ECOND S	EMESTE	R		13		
21T	Language – II	4	6		25	75	100	
22E	English – II	2	4	34-14	25	25	50 @	
23A	Core Paper II: Semiconductor Devices	4	5	- A	25	75	100	
23P	Core Practical I: Basic Electronics Lab	EDUCATE	ரரை <u>உ</u>	3	25	75	100	
23Q	Core Practical II: Semiconductor Devices Lab	4	1013333	3	25	75	100	
2AA	Allied : II Mathematics—II	4	5	-	25	75	100	
2FB	Value Education- Human Rights *	2	2	-	-	50	50	
2NM	Naan Mudhalvan – Skill Course	2	2	-	25	25	50 #	
Swatch Bhara	t- Summer internship \$							
	Total	26	T 24	L 06	175	475	650	

		THIRD	SEMESTE	R			
31T	Language – III	4	6	-	25	75	100
32E	English – III	4	4	-	25	75	100
33A	Core Paper III: Digital Principles and Applications	4	3	-	25	75	100
33B	Core Paper IV: Electronic Circuits	4	3	-	25	75	100
3AD	Allied: III Object Oriented Programming using C++	2	2	25	20	30	50
3ZA	Skill Based Subject : I Bio Medical Instrumentation	3	3	1/8	20	55	75
	Core Practical: III Digital Electronics and Linear IC's Lab - III			2	10115		-
	Core Practical: IV Electronics Circuits and Communication Lab -IV	No.		2	Silling	_	1
3FB / 3FC	Tamil **/ Advanced Tamil* (OR) Non-major elective - I (Yoga for Human excellence * Women's Rights*)	2	2	7 2	-	50	50
3NM	Naan Mudhalvan –Skill Course	2	2	371	25	25	50#
3HW	Health and Wellness**	1 En	CATTLEON	FURTE	25	-	25
	Total	26	T 26	L 04	190	460	650
		FOUR	 TH SEME	STER			
41T	I amaza a IV/	1	6				100
41T	Language – IV	4	6	-	25	75	100
42E	English – IV				25	75	100
43A	IC's and Instrumentation	4	5	-	25	75	100
4AD	Allied : IV Python Programming	4	5		25	75	100
43P	Core Practical: III Digital Electronics and Linear IC's Lab	3	-	2	30	45	75

43Q	Core Practical: IV Electronics Circuits and Communication Lab -IV	3	-	2	30	45	75					
43R	Allied Practical: V C++ Programming Lab	2	-	2	20	30	50					
4FB/ 4FE	Tamil**/ Advanced Tamil * (OR)	2	2	-	-	50	50					
	Non Major Elective-II (General Awareness *) Skill Based Subject :II						7 0.0					
4NM	Naan Mudhalvan –Skill Course	2	2	-	25	25	50#					
Total		28	T 24	L 06	205	495	700					
	FIFTH SEMESTER											
53A	Core Paper VI: 8051Microcontroller and its Applications	4	4	A CONTRACTOR	25	75	100					
53B	Core Paper VII: Internet of Things	4	4		25	75	100					
5EA/ 5EB/ 5EC/	Elective I	3	4		20	55	75					
5ED	55	5	w.	5/								
5EE/	Elective II					3						
5EF/	8	3	4	13/10	20	55	75					
5EG/	90	Mell	AR UN	300	CIGN.							
5EH	01/2		Coimbatore	1	6.6							
	Core Practical VI: 8051 Microcontroller Lab	இ _{ந்து}	பாரை	31118	- J	-						
	Core Practical VII: Internet of Things System Design Lab	-	TE TO ELE	3	-	-	-					
	Core Practical VIII: Industrial and Power Electronics Lab	-	-	3	-	-	-					
5NM	Naan Mudhalvan – Skill Course	2	2	-	25	25	50#					
5ZC	Skill based subject – III Visual Programming	3	3		20	55	75					
	Total	19	T 21	L 09	135	340	475					
-		•										

SIXTH SEMESTER										
63A	Core Paper VIII: Design with PIC Microcontroller	4	6		25	75	100			
6EI/ 6EJ/ 6EK/ 6EL	Elective III	3	6		20	55	75			
63P	Core Practical VI: 8051 Microcontroller Lab	4	-	3	25	75	100			
63Q	Core Practical VII: Internet of Things System Design Lab	4	-	3	25	75	100			
63R	Core Practical VIII: Industrial and Power Electronics Lab	4	-	3	25	75	100			
67V	PROJECT	2	2	-	20	30	50			
6ZP	Skill based Subject – IV Practical Visual Programming	2	க்கழ	3	20	30	50			
67A	Extension Activities**	2	2	-8	F (50	50			
6NM	Naan Mudhalvan – Skill Course	2	2	VA.	25	25	50#			
	Total	2 7	T 18	L 12	185	490	675			
l l	Gran <mark>d Total</mark>	144					3600			

^{*} No Continuous Internal Assessment (CIA). Only University Examinations

- @ University semester examination will be conducted for 50 marks (As per the existing pattern of examination) and the marks will be converted to 25 marks
- # Naan Mudhalvan Course :CEE will be assessed by Industry for 25 marks and CIA will be done by the course teacher

For Project report *30 marks and viva-voce *20 marks

@ English II- University semester examination will be conducted for 50 marks (As per existing pattern of Examination) and it will be converted for 25 marks.

^{**} No University Examinations. Only Continuous Internal Assessment (CIA).

^{\$} Swachh Bharat – Summer Internship. Extra 2 Credits would be given. It is mandatory.

	List of Elective papers (Colleges can choose any one of the paper as electives)						
	A	PCB Design and Fabrication					
Elective – I	В	Advanced Communication Systems					
C Artificial Intelligence							
	D	Advanced Computer Architecture					
	E	Arduino with Sensors					
Elective – II	F	Microwave and Fiber Optic Communication					
	G	Automotive Electronics					
	Н	Satellite Communications					

	Ι	Industrial and Power Electronics
Elective – III	J	Robotics
	K	Programmable Logic Controllers
	L	VLSI Design





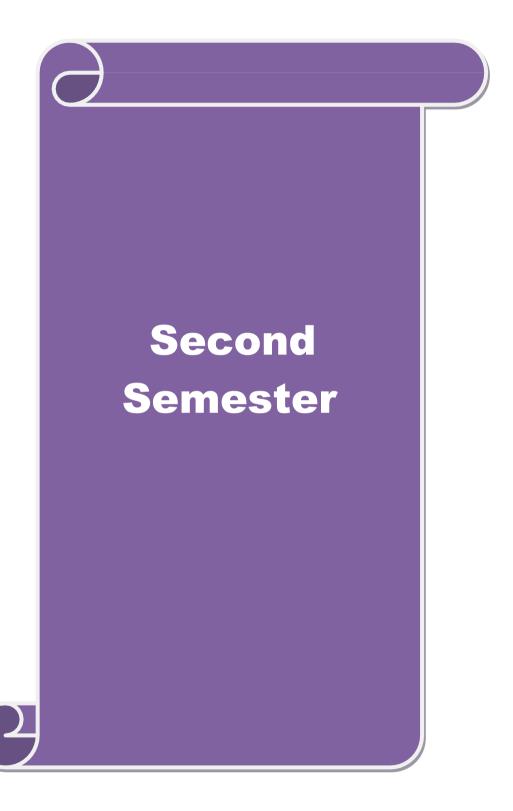
		BASIC ELECTRONICS	L	T	P	C			
Course code	13A								
Core /Ele		Core Paper I	5	T		4			
/ Suppo									
Pre-requ	ıisite	Higher secondary physics	Sylla		2025	-26			
			Vers	ion					
Course Objec									
· ·		is course are to:							
		with fundamentals of electronic components							
		amon electronic components							
		c circuits to perform realistic tasks							
Expected Cou									
		etion of the course, student will be able to:			K				
1 Understand the basic electronic components									
		electronic components			K				
3 Differenti	ate a <mark>nd den</mark>	nonstrate the voltage and current source.			K	3			
4 Apply the	electronic	components in network theorems.			K	3			
5 Put into p	ractice and	use the electronic components			K	4			
K1 - Rememl	oer; K2 - U	nderstand; K3 - Apply; K4 - Analyze; K5 - Evaluate	; K 6 – 0	Create	;				
	(575)	A 570-							
Unit:1		RESISTORS & INDUCTORS			hour				
		ed, Variable - Brief mention of their Construction	and Cha	aracte	ristic	es -			
		s - Connecting Resistors in Series and Parallel							
		<mark>d, Variable- Self and Mutual Inductanc<mark>e-Farad</mark>ay's l</mark>							
		ction-Energy Stored In An Inductor-Inductance In	Series A	And 1	Paral	lel-			
Testing of Res	sistance and	I Inductance using Multimeter.							
Unit:2	9	CAPACITORS		15	hou	rs			
Principles of	Capacitan	ce-Parallel Plate CapacitorEnergy Stored in a	Capaci	tor-T	ypes	of			
		Mica, Teflon, Ceramic, Plastic and Electrolytic							
Application-	Connecting	Capacitors i <mark>n Series and</mark> Parallel - Factors Gov	erning	the V	⁷ alue	of			
-	esting of Ca	apacitors Using							
Millimeters.		A BILLIAN WILLIAM							
Unit:3		ELECTRICAL ELEMENTS AND CIRCUITS	*** 1		hour				
		lectric Current-Electromotive Force - Ohms Law							
		Analysis of Resistance in Series Circuits, Parallel							
	-	t of Voltage Source and Current Source-Voltage S	Source 1	n Sei	ries a	and			
	e in Paralle	l-Simple Problems in DC Circuits.			_				
Unit:4		NETWORK THEOREMS			houi				
		- Thevenin Theorem - Norton's Theorem - T							
		of Voltage and Current Sources - Millman's Theo		tar ar	nd De	elta			
Conversion-M	iaximum Po	ower Transfer Theorem - Simple Problems in DC Cir	cuits.						
Timit.5	<u> </u>								
		AC CIRCUITS		15	ham	•C			
Unit:5	oSinusoidal	AC CIRCUITS	cictoros		hour	S			
IntroductionT		AC CIRCUITS IWave-RMSValue-AverageValue-ACCircuitswithRe - Circuits with XC Alone - Series Reactance And Res		;-		.s			

Reactance and Resistance - Series Parallel Reactance and Resistance - Real Power - Series

Resonant Circuit – Parallel Resonant Circuit - Passive Filters: Low Pass Filters, High
Pass Filters, Band Pass Filters, Band Rejection Filters
Total Lastuma harms 75 harms
Total Lecture hours 75 hours
Text Book(s)
1 S.Salivahanan, N.Suresh Kumar, A.Vallavaraj "ELECTRONIC DEVICES
AND CIRCUITS"-
2 B.V.Narayana Rao—PRINCIPLES OFELECTRONICS, Wiley Eastern Limited, 1992
Reference Books
1 B.L.Theraja, "BASIC ELECTRONICS-SOLID STATE DEVICES",S.Chand Company
2 BernardGrob "BASIC ELECTRONICS"-Tata McGraw-Hill Publishing Company Limited,
9thEdition.
Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]
1 https://nptel.ac.in/courses/108/104/108104139/
2 https://nptel.ac.in/courses/108/101/108101091/
3 https://www.youtube.com/playlist?list=PLFF553CED56CDE25D
4 https://www.youtube.com/watch?v=w8Dq8blTmSA
Course Designed By:
Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur &
S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mapping with Programme Outcomes										
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	S 2	S	M	L	L	L	L	M	S	M
CO2	L	L.	L	L	L	M	M	S	M	S
CO3	M	M	SS SS	L	M	S	L	L	M	M
CO4	M	L	L	DUCATE	10 EFE	AT.S	L	L	M	S
CO5	L	M	S	S	M	L	L	L	M	M

^{*}S-Strong; M-Medium; L-Low



Course code 23A	SEMICONDUCTOR DEVICES	L	T	P	C	
Core /Elective / Supportive:	Core Paper II:	5	Т		4	
Pre-requisite	Pre-requisite Higher secondary physics Syllabus Version 2					
Course Objectives:						
The main objectives of this	course are to:					
To enable the student	s to understand and gain the					
knowledge on semico	onductor devices.					
To acquaint the stude	nts with construction, theory and characteristics of	of the elec	troni	c		
Expected Course Outcome						
	ion of the course, student will be able to:			1		
1	of the basic electronic devices			K		
	eristics and operations of special diodes				2	
	eristics and operations of transistors				2	
	eristics and operations of FET and UJT			K		
•	for various applications			K	3	
K1 - Rememb <mark>er; K2 - U</mark> nc	lerstand; K3 - Apply; K4 - Analyz <mark>e; K5</mark> - Evalua	te; K6 - C	Create)		
Unit:1	PN JUNCTION DIODE			hou		
	Conduction in Insulator - Semiconductor, Con					
	Doping – P Type – N Type Semiconductor - For			Juncti	on	
Diode - Forward Bias - Reve	ers <mark>e Bias Condition – Characteristics - Clip</mark> ping a	ind Clamp	oing.			
Unit:2	SPECIAL DIODES			5hou	rs	
	stics – Breakdown - Backward Diode – Varactor		tep			
RecoveryDiode-PointContac	<mark>ctDiode-ShcottkeyDiode-TunnelDiode-</mark> GunnDio	de-				
ImpattDiode - PIN Diode - I	PNPN Diode	/				
Unit:3	Colimbia BJT			hour		
_	ction Transistor – Construction - Transistor Biasi			of N	PN	
	E &CC Configuration - Bias Stability - Load Line			T)	1	
•	etor to Base Bias - Voltage Divider Bias – Bias Co	ompensat	ion -	ınern	nai	
Runaway - HeatSink	THE AND HAVE	1	4.5	· •		
Unit:4	FET AND UJT	Duraitur Cla		hour		
	struction and Operation of N-Channel JFET -	Drain Un	атасі	erisii		
Comparison of JFET &BJ	Γ - Introduction to MOSFET - Enhancement 1	MOSFET	– D	epleti	ion	
Comparison of JFET &BJ	Γ - Introduction to MOSFET - Enhancement Mage Variable Resistor(VVR) - Introduction to U.	MOSFET	– D	epleti	ion	
Comparison of JFET &BJT MOSFET - FET as a Volta	Γ - Introduction to MOSFET - Enhancement Mage Variable Resistor(VVR) - Introduction to Unit - SCR – TRIAC-DIAC	MOSFET	– D racte	epleti	ion s –	
Comparison of JFET &BJT MOSFET - FET as a Volta UJT as Relaxation Oscillator Unit:5	Γ - Introduction to MOSFET - Enhancement Mage Variable Resistor(VVR) - Introduction to Unit - SCR – TRIAC-DIAC OPTOELECTROIC DEVICES	MOSFET JT – Cha	– Dracte	epleti ristic: 5hou :	ion s –	
Comparison of JFET &BJT MOSFET - FET as a Volta UJT as Relaxation Oscillator Unit:5 Principles, Operation And C	Γ - Introduction to MOSFET - Enhancement Mage Variable Resistor(VVR) - Introduction to Unit - SCR – TRIAC-DIAC	MOSFET JT – Cha – Photo I	Dracte1:Diode	epletiristics Shoulting Photographics - Photographics	ion s –	
Comparison of JFET &BJT MOSFET - FET as a Volta UJT as Relaxation Oscillator Unit:5 Principles, Operation And Comparison - Photo Voltaic Comparison of JFET &BJT William Set 1985.	T - Introduction to MOSFET - Enhancement Mage Variable Resistor(VVR) - Introduction to Unit - SCR - TRIAC-DIAC OPTOELECTROIC DEVICES Characteristics Of Opto Electronic Devices: LDR	MOSFET JT – Cha Photo I uum Phot	Dracte1:Diode	epletiristics Shoulting Photographics - Photographics	ion s –	

16	ext Book(s)
1	S. Salivahanan, N. Suresh Kumar, A. Vallavaraj, "ELECTRONICS DEVICES AND
	CIRCUITS", Tata McGraw Hill Publishing Company Limited, New Delhi, 8th edition.
2	B. L. Theraia, "BASIC ELECTRONICS – SOLID STATE DEVICES", S.Chand&Co

2 B. L. Theraja, "BASIC ELECTRONICS – SOLID STATE DEVICES", S.Chand&Company Ltd. 2000

Reference Books

- 1 S.L. Kakani, K. C. Bhan Dai ATEXTBOOK OF ELECTRONICS
- 2 BernardGrob"BASIC ELECTRONICS"-Tata McGraw-Hill Publishing Company Limited,

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

- 1 https://nptel.ac.in/courses/108/108/108108122/
- 2 https://nptel.ac.in/courses/108/108/108108112/
- 3 https://nptel.ac.in/courses/115/102/115102103/

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

				1 - 5 - 5	1 / 11	9 9			_	
Mappi	ng with	Program	nme Ou	tcomes	5 2					1
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	S	S	L	M	L	M	M	M	S	M
CO2	L	L	L	L	L	M	M	M	L	L
CO3	L	M	(2) (S) (S)	L	S	M	あり	L	M	M
CO4	L	M	M	FÓUCA	E TO E	EVATE	M	L	M	S
CO5	M	L	L	M	L	M	M	L	M	М

^{*}S-Strong; M-Medium; L-Low

Course code 23P	BASIC ELECTRONICS LAB	L	T	P	С
Core /Elective / Supportive:	Core Practical I	3		P	4
Pre-requisite	Basic Electronics theory	Sylla Vers		2025	-26

Course Objectives:

The main objectives of this course are to:

- ❖ To understand the fundamental principles of circuit theory
- To make use of circuit laws and theorems and measuring the circuit parameters.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

1	Apply the concept of basic circuit and theorems	К3
2	Simplify the circuits using series and parallel equivalents and using Thevenin's and	К3
	Norton's equivalent circuits.	
3	Design resonance circuits.	K4
4	Use the oscilloscope for the display and measurements of signals.	K2
5	Analyze Various Theorems with different resistance values	K4

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

BASIC ELECTRONICS LAB

90 Hours

(Any 12 Experiments)

- 1. Study of Multimeter Checking of Components
- 2. Measurement of Amplitude, Frequency & Phase Difference using CRO
- 3. Verification of Ohm's Law
- 4. Voltage sources in Series, Parallel and Series-Parallel
- 5. Resistance in Series, Parallel and Series-Parallel
- 6. Voltage and Current Dividers
- 7. Verification of Kirchhoff's Law
- 8. Wheatstone Bridge
- 9. Verification of Norton's Theorem
- 10. Verification of Thevenin's Theorem
- 11. Verification of Millman's Theorem
- 12. Verification of Superposition Theorem
- 13. LCR Bridge
- 14. Series Resonance Circuit
- 15. Parallel Resonance Circuit
- 16. Transient Response of RC Circuit
- 17. Transient Response of RL Circuit
- 18. Capacitors & Inductors in Series & Parallel
- 19. Frequency Response of R, L&C
- 20. Low Pass Filter & High Pass Filter
- 21. Band pass and Band Rejection Filter
- 22. Verification of Maximum Power Transfer Theorem
- 23. Measurement of resistance and capacitance in series and parallel

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

- 1 https://nptel.ac.in/courses/122/106/122106025/
- 2 https://nptel.ac.in/courses/122/106/122106026/

Course Designed By: K.Manikantan, Assistant Professor, Government Arts College, Ooty.& Dr.N.Om Muruga, Assistant Professor, Government Arts College, Ooty.

Mappi	ng with	Prograi	nme Ou	tcomes						
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	L	L	M	©L S	o L)	L	L	M	S	M
CO3	L	M	M	M	L	M	L	S	M	L
CO3	M	L	S.	La	L	S	L	L	L	M
CO4	M	M	L	L		S	M	L	M	L
CO5	L	M	S	M	M	M	L	L	M	M

*S-Strong; M-Medium; L-Low

Coimbetore மூர்த்தி

Course code 23Q	SEMICONDUCTOR DEVICES LAB	L	T	P	C
Core /Elective / Supportive:	Core Practical II	3		P	4
Pre-requisite:	Higher secondary physics	Sylla Vers		2025	-26

Course Objectives:

The main objectives of this course are to:

- ❖ To understand and experiment the basic parameters of electronic devices.
- ❖ To construct few applications using semiconductor devices.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

	1	
1	Experiment the fundamental operations of the main semiconductor electronic devices.	K3
2	Design and construct electronic circuits using semiconductor devices.	K3
3	Understand the transistor characteristics	K2
4	Understand the characteristics of LDR and solar cell	K2
5	Apply Various transistor characteristics in applications.	К3

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

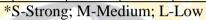
UNIT I	SEMICONDUCTOR DEVICES LAB	90 Hours

(Any 12 Experiments)

- 1. Band Gap Energy of Silicon / Germanium Diode
- 2. V-I Characteristics of Junction Diode
- 3. V-I Characteristics of Zener Diode
- 4. Transistor Characteristics of CE Configuration
- 5. Transistor Characteristics of CB Configuration
- 6. Transistor Characteristics of CC Configuration
- 7. Clipping Circuits
- 8. Clamping Circuits
- 9. Measurement of Stability Factor of Fixed Bias
- 10. Measurement of Stability Factor of Self Bias
- 11. V-I Characteristics of JFET
- 12. V-I Characteristics of UJT
- 13. UJT as Oscillator
- 14. Characteristics of Solar Cell
- 15. Study of IR(Tx&Rx)
- 16. Study of LED and 7Segment display
- 17. Temperature Co-efficient of Junction Diode
- 18. Zener as a Voltage regulator
- 19. ON / OFF control of relay using Opto-Couplers
- 20. Characteristics of SCR
- 21. TRIAC Characteristics

Course Designed By: K.Manikantan, Assistant Professor, Government Arts College, Ooty.& Dr.N Om Muruga, Assistant Professor, Government Arts College, Ooty.

			Mapp	ing with	Progra	mme O	utcomes			
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	L	L	M	L	L	L	L	M	S	M
CO3	L	M	M	M	L	M	L	S	M	L
CO3	M	L	S	L	L	S	L	L	L	M
CO4	M	M	L	L	L	S	M	L	M	L
CO5	L	M	S	M	M	M	L	L	M	M







Course code	33A	DIGITAL PRINCIPLES AND APPLICATIONS	L	Т	P	C	
Core /Ele	ective /	Core Paper III	3	Т		4	
Suppor	tive:					_	
Pre-req	uisite:	Basic Physics	Basic Physics Syllabus 20 Version			2025-26	
Course Objec	tives:			Į.			
To acquTo outling sequential	ire the basione the form al circuits	is course are to: c knowledge of Number system, Digital logic circuits a all procedures for the analysis and design of combination ts of A/D, D/A conversions and their types			catio	n	
On the success		etion of the course, student will be able to:					
	-	s of Number system and gates			l K	2	
		of various logic gates and analyzing the outputs			K		
		he combinational logic circuits			K		
·		he Sequential logic circuits				4	
		ronous and asynchronous sequential circuits				6	
		nderstand; K3 - Apply; K4 - Analyze; K5 - Evaluate;	K6	reate			
Unit:1		NUMBER SYSTEMANDCODES		Access	houi	rs	
and Multiplicat	tion – 1's ar	d Hexa Decimal Numbers – Conversion — Binary Add 2's Compliments - Binary Coded Decimal (BCD) – Excess Three – Grey Code .					
Unit:2	CA.	BOOLEAN ALGEBRA AND LOGIC GATES		12	2hou	rs	
– Sum of Produ	acts and Pro	- Boolean functions — Truth Tables — Basic Laws — Deoducts of Sums — Karnaugh map —Universal Gates - Log R, EX-OR and EX-NOR Gates .	_			m	
Unit:3		COMBINATIONALLOGICCIRCUITS		12	hour	S	
		 Half Sub tractor – Full Sub tractor – 4 bit Binary A Demultiplexer – Decoders – Encoders – Parity Gene 					
Unit:4		SEQUENTIALLOGICCIRCUITS		12	hour	'S	
Flip Flops – RS	Counters – I	RS, JK, JK Master Slave, D and T Flip Flops – Shift R Ripple Counters – Synchronous Counter – Up Down co	_				

Unit:5	D/A AND A/D CONVERTERS	12 hours
Digital to	Analog Converters: Resistive Divider Type - Ladder Type - Accuracy and Re	solution -
Analog to	Digital Converters: Counter-simultaneous Conversion - Dual Slope Type -	Accuracy
and Reso	lution.	
	Total Lecture hours	60
		hours
Text Boo	ok(s)	
	Malvino & Leech, —Digital Principles and Applications", Tata McGraw Hi	11
1	Edition V, 2002.	
2	M.Morris Mano , Digital Logic and Computer Design", PHI 2005.	
	Reference Books	
1	Floyd and Jain, Dig<mark>ital Fundamentals, Prentice Hall2010</mark>	
	M. Morris Mano Charles Kime, Digital Logic and Computer Design Funda	nentals,
2	Pearson Education Limited, 2014	
Related (Online Contents [MOOC, SWAYAM, NPEL, Website etc.]	
1	https://soaneemrana.org/onewebmedia/DIGITAL%20PRINCIPLES%20AND9	<u>%20APPL</u>
	ICATION%20BY%20LEACH%20&%20MALVINO.pdfE book, Malvino & 1	Leech,
	—DIGITALPRINCIPLESANDAPPLICATIONS, TataMcGrawHillEdition	nXI,2011
2	https://nptel.ac.in/courses/117/106/117106086/Introduction to digital circuits	
	Course Designed By:	
	Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Ti	rupur &
	S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.	

									Para .	
Mappi	ng with P	rogra <mark>mn</mark>	ne Outo	comes					19	
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
		90		Mil	AD					
CO1	S	Sog	S	M	M	M	S	L)	L	S
			3/3		Continue	110111				
CO2	S	S	S	M	M	M	S	L	L	L
				³ /55	LILITER	NIT 2_11	The same			
CO3	S	S	S	M	M	ELLAT	L	S	S	M
					116 10	77				
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	S	L	M	M	S	S

^{*}S-Strong; M-Medium; L-Low

Course Code	33B	ELECTRONIC CIRCUITS	L	Т	P	С
Core /Elective / Supportive:	Core pap	er IV	3	Т		4
Pre-Requisite:		Basic Physics		abus sion	2025-	26

Course Objectives:

The Main Objectives of this course are to:

- ❖ To enable the students to understand and gain the knowledge on power supplies, amplifiers and oscillators.
- ❖ To acquaint the students with construction, theory and characteristics of the electronic amplifier circuits and types of multivibrators.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

	1	Understand the concepts of Rectifiers and regulators	K2
Ī	2	Study about Small signal amplifiers	K1
Ī	3	Analyze the functions of power amplifiers	K4
Ī	4	Analyze the performance of negative as well as positive feedback circuits	K4
	5	design oscillators and Multivibrators	K6

K1:Remember; K2-Understand; K3-Apply; K4-Analyze; K5-Evaluate; K6-Create
Unit:1 RECTIFIERS AND REGULATORS 12 hours

Half wave, Full waves and bridge Rectifiers – Calculation of RMS Value – Average Value – Ripple Factor – Efficiency – Transformer Utility Factor – Peak Inverse Voltage – Inductor Filter – Capacitor Filter – LC Filter - Voltage Doubler – Voltage Regulator – Construction of DC Power Supply.

Unit:2 SMALL SIGNAL AMPLIFIERS 12 hours

CE, CB, CC amplifiers— Current Gain - Voltage Gain — Power Gain — Single Stage Transistor Amplifier — DC and AC load line — RC Coupled Amplifier — Transformer Coupled Amplifier.

Unit:3 POWER AMPLIFIERS 12 hours

Operation and Graphical Representation of Class A, Class B, Class C and Class AB Amplifiers – Maximum Collector Efficiency of Class A Power Amplifier – Harmonic Distortion – Class B Push Pull Amplifier .

Unit:4	FEEDBACK AMPLIFIERS	12
		hours

Basic concepts of feedback – Positive Feedback – Negative Feedback – Effects of Negative Feedback on Gain, Bandwidth and Distortion – Noise – Voltage Series Feedback - Voltage Shunt Feedback – Current Series Feedback – Current Shunt Feedback.

Unit:5	OSCILLATORS AND MULTIVIBRATORS	12 hours						
Barkh	lausen Criterion – Hartley Oscillator – Colpitts Oscillator – Phase Shift Osc	cillator –						
Wein Bi	ridge Oscillators - Astable Multivibrator - Monostable multivibrator -	Schmitt						
Trigger								
	Total Lecture hours	60 hours						
Text Boo	ok(s)							
1	S.K.Sahdev, Electronic Principles ", Dhanpat Rai & Co(P) Ltd, 2nd Edition, 19	998						
2	B.L.Theraja, "Basic Electronics", Chand Company Ltd, 2000.							
Referen	ce Books							
1	V.K.Metha, Rohit Metha, —Principles of Electronics" S Chand, 2006.							
2	B.Sasikala, C.Poornachandra, Electronic Devices and Circuits", Scitech	2003.						
Related	Online Contents [MOOC, SWAYAM, NPEL, Website etc.]							
1	http://www.ee.iitm.ac.in/~ani/2012/ec5135/lectures.htmlLecture Notes							
2	https://nptel.ac.in/courses/108/102/108102097/#Introduction to Electronic	circuits						
	NPTEL.							
3	https://nptel.ac.in/courses/108/102/108102095/Analog Electronic circuits N	VPTEL.						
1	Course Designed By:							
E	Pr.K.Ve <mark>nmathi ,Assistant professor,</mark> L.R.G.Go <mark>vt Arts C</mark> oll <mark>ege for W</mark> omen, T	irupur &						
	Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Ero	de.						

Mappi	ng with P	rogra <mark>mn</mark>	ne Outo	comes		100		(6)		
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
		0)9						0,6		
CO1	S	S	M	M	M	M	S	M	M	S
		1	\$ 6				5			
CO2	S	M	M	M	M	₂ M	S	L	L	L
				DUCA	TE TO EL	EVATE				
CO3	S	S	S	M	M	L	L	L	M	M
				EDU	ATE TO	EI EVAT				
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	M	L	M	M	S	M

^{*}S-Strong; M-Medium; L-Low

Cours	e Code	3AD	OBJECT ORIENTED	L	Т	P	С
Cours	e Coue	JAD	PROGRAMMING USING C++		1	1	
Core /	Elective /		ALLIED PAPER III				
	ortive:		ADDIED I AI EK III	3	T		3
	quisite:		Computer Fundamentals	Syll	abus	2025-	-26
	•		•	_	sion		
Course O	bjectives:					I.	
The Mair	o Objective	es of this c	ourse are to;				
			the programming concepts of Object				
			g using C++.				
⋄ To	develop a	skills on u	sing MS word, MS Excel, Power Point ar	nd MS	Acces	SS	
Expected	Course O	utcom <mark>es:</mark>	NEW TO				
On su	ccessful co	mpletion o	of this subject the students should have :V	Vriting	progr	ammiı	ng
ability on	OOPS cond	c <mark>epts like</mark> l	E <mark>ncapsulation, Data abstracti</mark> on <mark>, Inher</mark> itan	ce, Pol	lymor	phism	and
Exception	handling <mark>e</mark>	tc.,					
1	Recognize	and under	stand Basic of Computer				K2
2		All the second	Word Processing				K3
3	Use and P	ractice of	MS Excel				K3
4	Knowledg	e to Make	Small Presentation				K6
5	Use and Pa	ractice of l	MS Access			1	K3
K1:Rem	embe <mark>r; F</mark>	C2-U nders	tan <mark>d; K3-App</mark> ly; K4-Anal <mark>yze</mark> ; K <mark>5</mark> -Ev <mark>a</mark>	ıluate;	K	6-Crea	te
Unit:1		la.	INTRODUCTION TO C++			9 ho	urs
Basic Cond	cepts of OOI	Ps – Advan	tages OOPs- I/O in C++ - C++ Declarations	- Contr	ol Stru	ctures	
Decision M	Taking State	ments – If.	Else – Jump – GOTO – Break – Continue – oWhile loops – Array –Types of array.				
Unit:2	8		CLASSES AND OBJECTS	19	1	9 ho	urs
	90		HIAD HINN	\$6			
		4/6	s – Function Overloading-Declaring objects -		•		
			es and functions – Array of objects – Friend f	unction	ıs – Ov	erload	ing
member fu	nctions – Co	onstructor a	nd Destructors. – String Functions.				
			த்தப்பாரை உய				
Unit:3		OPERAT	OR OVERLOADING & INHERITANCE			9 ho	urs
Overloadin	ıg unary, Bir	nary operate	ors – Overloading friend functions – Type co	nversio	n - Inh	eritanc	e:
Types of	inheritance:	Single, Mu	ltilevel, Multiple, Hierarchical, Hybrid and M				
Virtual B	ase classes -	- Abstract (Classes.				
	1						
Unit:4			POINTERS			9 ho	urs
Declarati	on _ Pointor	to class of	oject – THIS pointer – Pointer to derived clas	sac and	hasa a	laccac	
			te operators – Dynamic objects – Binding, Po				
functions		and dole	2 James of the Bridge Filling, I	,		Y	
	•						

Unit:5	EXCEPTION HANDLING & FILES	9 hours							
	Exception handling -File stream classes – File Modes – Sequential read/write operations – Binary nd ASCII files –Random Access File.								
	Total Lecture hours	45 hours							
Text Bo	ok(s)								
1	Ashok N Kamthane: Object Oriented Programming with ANSI and Turbo Pearson Education Publ., 2003.	shok N Kamthane: Object Oriented Programming with ANSI and Turbo C++, earson							
Referen	ce Books								
1	E. Balagurusamy: Object Oriented Programming with C++, TMH Pub., 19	998.							
2	Maria Litvin and Gary Litvin: C++ for you++, Vikas Publ, 2002								
3	John R Hubbard: Programming with C++, TMH Publ. II Edition, 2002								
Ε	Course Designed By: Or.K.Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, T Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Ero	*							

Mappi	ng with P	<mark>rogr</mark> amn	ne Outo	comes	R		75	(0)		
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
			100	wie	200 00	?				
CO1	S	S	M	M	M	M	S	M	M	S
		1		5	Z	(1			
CO2	S	M	M	M	M	M	S	L	, L	L
			160						(S)	
CO3	S	S	S	M	M	L	L	L	M	M
		000		Y				000		
CO4	M	M	M	S	S	S	L	L	M	M
			158				113			
CO5	M	M	S	S	M	L	M	M	S	M
				Fin	JUI 60) []				

^{*}S-Strong; M-Medium; L-L

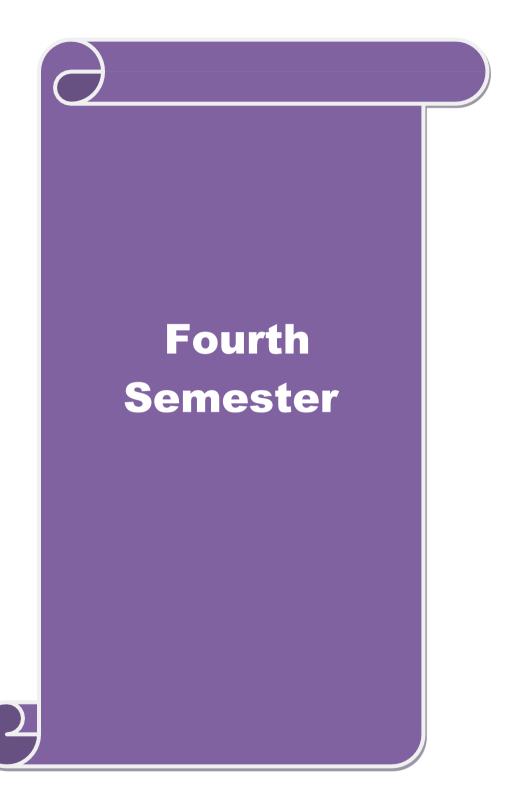
Course code	3ZA	BIOMEDICAL INSTRUMENTATION	L	T	P	С
Core /Ele Support		Skill Based Subject: I	3	Т		3
Pre-req	uisite	Higher secondary biology	Sylla Vers	bus 2	025	-26
Course Object						
The main object	ctives of thi	s course are to:				
-		bio-potentials and working principles of medical inst nts to learn about bio-potentials and medical instrume		S		
Expected Cou	rse Outcor	mes:				
On the succes	sful comple	et <mark>ion</mark> of the <mark>course, student</mark> will be able to:				
1 Understan	d the Conc	ept of bio-potential			K	2
2 Understan	d the conce	ept of medical instruments			K	2
		nooting Skills of medical instruments			K	3
•		epts of signal conditioners & diagnostic equipment			K	
		al assist devices			K	4
	•	nderstand; K3 - Apply; K4 - Analyze; K5 - Evaluate;	K6 – (reate		
Transmembran		BASIC PHYSIOLOGY - Transport of Ions through Cell Membrane - Resting - Nervous System - Physiology of Muscles - Heart an		cited of Circ	culat	e ion
Unit:2		ELECTRODES AND TRANSDUCERS	7 11		our	
Equivalent Cir Electrode - Blo	cuit - Electord Gas Electord	- Micro Electrodes - Skin Surface Electrodes - I trode Materials - Chemical Electrodes - Reference I ectrode - Thermistor	Electro	des - '	The	рН
Unit:3	SIGNAL (CONDITIONERS & DIAGNOSTIC EQUIPMENT	a'S		9 ho	ours
Pass, High Pass	s and Band AGNOSTIC	rs - Current Amplifiers - Isolation Amplifier - Need for Pass Active Filters - Notch Filters - Heated Stylus and EQUIPMENTS: Typical Electrocardiogram (ECG)-EEG).	d Ink P	en		oh
Unit:4	DIAGN	OSTIC EQUIPMENTS & BIOTELEMETRY		9 h	ours	;
BIOTELEMET	TRY AND	uoroscopy - Image Intensifiers - Angiography - Endo PATIENT SAFETY: Need for Biotelemetry - Ele System - Physiological Signals used in Telemetry .				-

Un	it:5	PHYSIOLOGICAL ASSIST DEVICES	9 hours
Need	d for Pace	makers - Pacemaker Parameters and Circuits - Different Mode	es of Operation - DC
		- Heart Lung Machines - Nerve and Muscle Stimu NS: Analysis of ECG signals .	lator. COMPUTER
		Total Lecture hours	45 hours
Tex	xt Book(s)		
1		Carr and John M. Brown, "Introduction to Biomedical Equip	ment
	Technol	<u> </u>	
2		Cromwell.,FredJ. Webell., Erich A. Pfeffer.,"Bio-m	edical
	Instrum	entation	
Ref	ference Bo		
1	Khandpu New	r, "Handbook on Biomedical Instrumentation", Tata McGray	v Hill Company,
2		Tebster, Ed., "Medical Instrumentation Application and Designment & Sons, Singapore, 1999	gn", Third Edition,
3	Arumuga	m <mark>.M,"BiomedicalInstrumentation",</mark> AnuradhaAgenciesPublish	ners,Chennai,1992
1	4		
Rel	lated Onli	ne Contents [MOOC, SWAYAM, NPTEL, Websites etc.]	
1	https://ww	w.youtube.com/watch?v=i2mZylgP1Fk	
2	https://ww	w.youtube.com/watch?v=4ldv98F7Zng	
3	https://npt	el.ac.in/courses/108/105/108105101/	
4	https://npt	el.ac.in/courses/108/105/108105091/	
·		Course Designed By:	/-
	Dr.K.	Venmathi ,Assistant professor,L.R.G.Govt Arts College for Wor	nen, Tirupur &

Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	Mapping with Programme Outcomes TO ELEVA											
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10		
CO1	L	L	M	M	M	L	L	M	S	M		
CO2	L	M	M	L	L	M	L	S	L	S		
CO3	M	L	S	L	M	L	L	M	M	L		
CO4	L	L	S	M	S	S	L	S	M	L		
CO5	L	L	S	S	M	L	S	L	M	S		

^{*}S-Strong; M-Medium; L-Low



Course code	ode 43A IC'S AND INSTRUMENTATION		L	T	I	C
Core /Elec	tive /	Core paper V	5	Т		1
Supporti	ve:		3	1		-
Pre-requ	isite	Basic Electronics	Syllab Version		202	25-26
Course Object	tives:					
	electror To enat transdu	art the knowledge on IC fabrication, Timer, PLL, and hic instruments ble the students to acquire the knowledge of Op-amp., cers and its applications in electronic circuits and know mique of measurements using electronic instruments				
Expected Cou	rse Ou	tcom <mark>es:</mark>				
On the succes	ssful co	mpletion of the course, student will be able to:				
1 Recognize	e the sta	ndards in IC Fabrication Technology				K1
2 Understar	nd the w	orking of Timer and PLL				K2
3 Design sin	mpl <mark>e cir</mark>	cuits using Op-amp.				K3
		rinciple of various types of transducers K2 Study the working of frequently used equipment's like CRO				K4
5 Digital V	oltm <mark>ete</mark> r	etc.				K5
K1 - Remem	ber; K2	- Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate;	K6 – (Crea	te	
Unit:1		IC FABRICATIONTECHNOLOGY			12	2hours
Epitaxial Metalliza Technolo	Growthation–M	Monolithic IC Technology – Basic Planar Process- Wafe Oxidation—Photolithography—DiffusionofImpurities—Iso onolithic Transistors—Integrated Resistors—Thin and Thic	olation '	Гесһ		ues–
Unit:2	6	TIMERANDPLL			12	2hours
Ramp Generato Trigger – FSK (or — Puls Generate	ram of 555 timer – Monostable Operation – Applications se Width Modulator – Astable Operation – Application or - Phase Locked Loop: Functional Block Diagram – Ph-Voltage Controlled Oscillator – Low Pass Filter .	s: Schr		1	
Unit:3		OPERATIONALAMPLIFIER			12	2hours
Summing Am Instrumentation	plifier Amplif	Inverting and Non-inverting Amplifier – Op-amp Par – Difference Amplifier – Integrator – Difference – Current to Voltage of Early – Wein Bridge Oscillator–Square Wave and Triangular	ntiator Conver	– ter	erato	or.
Unit:4		TRANSDUCERS				2hours
of Transducers Thermistors –	SelectionThermo	al Transducer – Basic requirements of Transducer – Claction of Transducers – Resistive Transducers – Potenticouple – Piezoelectric Transducers – Hall Effect Transducers – Digital Displacement Transducers.	ometer	s –	•	

Unit:5		12
	ELECTRONIC INSTRUMENTS	hours
Q Me	eters- CRO: Block Diagram - Cathode Ray Tube - Measurement of Frequence	cy –
	ment of Voltage and Current - Digital Oscilloscope - Digital voltmeter- I	
_	ntegrating Type DVM - Digital Multimeter - Humidity Measuremen	t –
Measurer	ment of PH.	
		60
	Total Lecture hours	60
		hours
Text Boo		
	D.Roy Choudhury and Shahil B Jain, Linear Integrated Circuits", Second Ed	lition
	New Age International Publishers, 2004	
2	K.R.Botkar,—Integrated Circuits", 10th Edition Khanna Publishers, 2006	
Reference	ee Books	
	J.B.GUPTA, A Course In Electronic and Electrical Measurements and	
1	Instrumentation", 12th Edition, S.K Kataria & Sons.	
2	A.K. Sawhne <mark>y, ELECTRICAL & ELECTRONIC MEASUREMENTS AN</mark>	ID
	INSTRUMENTATION:, Dhanpath Rai & Co (P) Ltd, 2004.	
Related (Online Contents [MOOC, SWAYAM, NPEL, Website etc.]	
1	https://nptel.ac.in/courses/108/108/108108111/Integrated circuits,opamps and t	heir
	applicat <mark>ions</mark>	
2	https://nptel.ac.in/courses/117/106/117106030/Analog IC Design	
	Course Designed By:	7
D	r.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupu	ur &
	Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode	

Mappi	Mapping with Programme Outcomes											
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10		
		(V)			Colmb	dote 1		G.	/			
CO1	S	S	M	M	M	M	S	M	M	S		
			6/	3)		- 11	いあり					
CO2	S	M	M	M	M 60	M	S	L	L	L		
				EDU	ATE TO	ELEVAL						
CO3	S	S	S	M	M	L	L	L	M	M		
CO4	M	M	M	S	S	S	L	L	M	M		
CO5	M	M	S	S	M	L	M	M	S	M		

^{*}S-Strong; M-Medium; L-Low

Course code	4AD	PYTHON PROGRAMMING	L	T	P	C
Core/Elective / Supportive		ALLIED PAPER IV	5	Т		4
Pre-requisite		Knowledge on logic of the programs and oops Concept.	Syllab Versio		2025	-26

Course Objectives:

The main objectives of this course are to:

- 1. To introduce the fundamentals of Python Programming.
- 2. To teach about the concept of Functions in Python.
- 3. To impart the knowledge of Lists, Tuples, Files and Directories.
- 4. To learn about dictionaries in python.
- 5. To explore the object-oriented programming, Graphical programming aspects of python with help of built in modules.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

1	Remembering the concept of operators, data types, looping statements in Python	K1
	Programming.	
2	Understanding the concepts of Input / Output operations in file.	K2
3	Applying the concept of functions and exception handling	К3
4	Analyzing the structures of list, Tuples and maintaining dictionaries	K4
5	Demonstrate significant experience with python program development environment	K4-K6

K1-Remember; K2-Understand; K3-Apply; K4-Analyze; K5 -Evaluate; K6 -Create

Unit:1 BASICS OF PYTHON 10hours

BASICS: Python - Variables - Executing Python from the Command Line - Editing Python Files - Python Reserved Words - Basic Syntax-Comments - Standard Data Types - Relational Operators - Logical Operators -Bit Wise Operators-Simple Input and Output.

Unit:2 CONTROL STATEMENTS 10hours

CONTROL STATEMENTS: Control Flow and Syntax - Indenting - if Statement - statements and expressions- string operations- Boolean Expressions -while Loop - break and continue - for Loop.**LISTS**:List-listslices-listmethods-listloop–mutability–aliasing-cloninglists-

List parameters. TUPLES: Tuple assignment, Tuple as return value-Sets-Dictionaries

Unit:3 FUNCTIONS 10hours

FUNCTIONS: Definition - Passing parameters to a Function - Built-in functions- Variable Number of Arguments. **Libraries in Python**: Tensor Flow, Matplotlib, Open CV, Pandas, NumPy, SciPy, Scrapy, PyTorch,

Scikit Learn.

Unit:4 ERROR HANDLING 12hours

ERROR HANDLING: Run Time Errors - Exception Model - Exception Hierarchy – Handling Multiple Exceptions-Handling IO Exceptions. **Data Streams**-Access Modes Writing-Data to a File Reading- Data From a File - File Methods – Working with Directories.

	nit:5	OBJECT ORIENTED FEATURES	10hours
		IENTED FEATURES: Classes Principles of Object Orientation - Ca	
Ins	tance Meth	ods-Class Variables-Inheritance - Polymorphism - Regular Expression	ons.
	nit:6	Contemporary Issues	3hours
E	xpert lectur	es, online seminars -webinars	
		Total Lecture hours	55h o 11 mg
/ID	4D - 1 (a)		55hours
-	extBook(s)		D=41
1		nmer field, Programming in Python3:AComplete introduction to the F	ytnon
2		e, Addison-Wesley Professional, 2009.	
2		Brown, PYTHON: The Complete Reference, McGraw-Hill, 2001 arusamy (2017), "Problem Solving and Python Programming", McGraw-	yy Hill Einst
3	E. Balage Edition.	irusamy (2017), Problem Solving and Python Programming, McGia	iw-fill, filst
	Edition.		
R	eference B	ooks	
			d adition Thedata
1		Downey, "Think Python: How to Think Like a Computer Scientist", 2 ⁿ n 3, Shroff /O' Reilly Publishers, 2016	edition, Update
2		n Ross <mark>um and Fr</mark> ed L. Drake Jr, An Introduction to Python – Revised 13.2, Network Theory Ltd., 2011	and updated
3	Wesley J	Chun, Core Python Applications Programming, Prentice Hall, 2012.	
R	elated Onl	ine Contents [MOOC, SWAYAM, NPTEL, Websites etc.]	
1	Python for	Everybody-Specialisation - Coursera	
2	Learn Pyth	non: The Complete Python Programming Course - Udemy	
3	Python for	Data Science – NPTEL / Swayam	7 7
		96 F/AD 11 S	
Co	ourse Desig	gned By:	
	Ī	Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Wor	
		Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, E	rode.
		SSULITION 2 LINE	

Mappi	Mapping with Programme Outcomes											
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10		
CO1	S	S	S	L	S	M	L	M	S	S		
CO2	S	S	S	L	S	M	L	M	S	S		
CO3	S	S	S	L	S	M	L	M	S	S		
CO4	S	S	S	L	S	M	L	M	S	S		
CO5	S	S	S	L	S	M	L	M	S	S		

^{*}S-Strong; M-Medium; L-Low

Cor	ırse Code	43P	DIGITAL ELECTRONICS AND LINEAR IC's LAB	L	Т	P	С
			221 (2121 10) 2.12				
	/Elective		Core Practical III	2		P	3
	upportive: Requisite:	Digital	Electronics and Microprocessor theory	Sylla	hug	2025-20	6
Pre-	Requisite:	Digital	Electronics and Microprocessor theory	Vers		2025-20	J
Course	Objectives	:		_			
he M	ain Objecti	ves of this	s course are to:				
*	To understa	nd the log	cical operation of various gates and theorems				
	-		gital circuits				
			bly language programming of Microprocessor	and to inte	erface		
	it with vario	ous periph	eral devices				
Expect	ed Course (Outcomes	600000000000000000000000000000000000000				
			of the course, student will be able to:				
		_	ing Boolean laws			H	Κ6
			subtractor circuit using logic gate			I	ζ6
			ithmetic Programming of 8085			F	Κ6
4	Knowledge	ab <mark>out log</mark>	ic Programming of 8085			F	Κ 6
5	Understand	th <mark>e w</mark> ave	form generation	4		I	K 1
K1 :R6	emember;	K2-Unde	rstand; K3-Apply; K4-Analyze; K5-Evalua	te; K6 -	-Create		
Part 1			DIGITAL ELECTRONICS (Any 12 Exp	p <mark>erime</mark> nts)	40 H	ours
	1	Verification	on of Basic Gates and Universal gates	7			
			n of Demorgan's Theorem				
	3 H	Ialf Add <mark>e</mark> 1	and Full Adder & Half Subtractor and Full Sul	otractor			
	4 E	ecade Co	unter &BCD Counter	S	7 1		
	5 S	tudy of Fl	lip- <mark>Flops</mark>	6			
		1	and De-Multipl <mark>exers</mark>	67			
			d Decoder				
			egmentDisplay				
		•	rey code and Grey to Binary code				
		,	s and Asynchronous Counter Simulation of Logic Gate using VHDL Coding				
		_	Simulation of Adder Circuits using VHDL Coding	inσ			
D4 I				<u>.</u>		45	
Part I	1		LINEAR IC'S LAB			45 (ours
	Astable Mul		_				
			ratorusing555				
	_		verting Amplifier				
	Phase Shift						
_	Instrumenta	_					
6. 7	-		onverter and Current to Voltage Convertor				
	_		or usingIC741 angular Generator				
ο.	Square wav	c and 111	angular Ocherator				

9. Adder and Subtractor usingIC741

	Total Lecture hours	85 hours							
Course Designe	d By:								
Dr.K	Dr.K. Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr.								
S	Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.								

Mapping wit	Mapping with Programme Outcomes												
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10			
CO1	S	S	M	M	M	M	S	M	M	S			
CO2	S	M	M	M	M	M	S	L	L	L			
CO3	S	S	S	M	M	≪L,	L	L	M	M			
CO4	M	M	M	S	S	S	L	L	M	M			
CO5	M	M	S	S	M	L	M	M	S	M			

^{*}S-Strong; M-Medium; L-Low



Course Code	43Q	ELECTRONIC CIRCUITS & COMMUNICATION LAB	L	T	P	С
Core /Elective / Supportive:		Core Practical IV	2		P	3
Pre-Requisite:	Electronic circuits theory and Communication theory			abus sion	2025	-26

Course Objectives:

The Main Objectives of this course are to:

- To understand the concept of working of regulated power supplies, rectifiers, amplifiers and oscillators.
- * To experiment the modulation and detection techniques.

Expected Course Outcomes:

On the Successful completion of the course, student will be able to:

	•	
1	sign power supply and rectifier circuits	K6
2	sign Amplifier circuits	K6
3	sign different Oscillator circuits	K6
4	sign different Modulation circuits	K6
5	Study on communication antennas	K1

K1:Remember; K2-Understand; K3-Apply; K4-Analyze; K5-Evaluate; K6-Create

Part I ELECTRONIC CIRCUITS (Any 12 Experiments) 45 hours

- 1. DC Regulated Power Supply using Zener Diode
- 2. Voltage Doubler
- 3. Feedback Amplifier
- 4. Emitter Follower
- 5. Transformer Coupled Amplifier
- 6. Hartley Oscillator
- 7. Colpitts Oscillator
- 8. Phase shift Oscillator
- 9. Wein Bridge Oscillator
- 10 RC Coupled Amplifier
- 11. Half Wave and Full Wave Rectifier
- 12 Filter Circuits

	ELECTRONIC COMMUNICATION	40
Part II		hours

- 1. Performance of IF Amplifier
- 2. AM Modulation and Detection
- 3. FM Modulation and Detection
- 4. PAM Modulation
- 5. PIN Diode Oscillator
- 6. Alignment of Dish Antenna
- 7. Alignment of Satellite Receiver
- 8. PWM Modulation
- 9. PPM Modulation
- 10. PCM Modulation
- 11. GUNN Diode Oscillator

Total Lecture hours	85
	hours

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	Mapping with Programme Outcomes									
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	S	M	M	S
CO2	S	M	M	M	M	M	S	L	L	L
CO3	S	S	S	M	M	L	C ^L	L	M	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	M	L	M	M	S	M

*S-Strong; M-Medium; L-Low

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Significant Pulitipish

EDUCATE TO ELEVATE

Course code 43R	OBJECT ORIENTED PROGRAMMING USING C++ & PYTHON PROGRAMMING LAB	L	T	P	C
Core/Elective/Supportive	Allied Lab V	2		P	2
Pre-requisite	Basics of any Programming Language	Syllabus Version 2025-20		-26	

Course Objectives:

The main objectives of this course are to:

- 1. This course presents an overview of elementary data items, lists, dictionaries, sets and tuples
- 2. To understand and write simple OOP WITH C++ Python programs
- 3. To Understand the OOPS concepts of Python
- 4. To develop web applications using Python

Expected Course Outcomes:					
On the successful completion of the course, student will be able to:					
1 To write programs in Python using OOPS concepts	K1,K2				
2 To understand the concepts of File operations and Modules in Python	K2,K3				
3 Implementation of lists, dictionaries, sets and tuples as programs	K3,K4				
4 To develop web applications using Python	K5,K6				
K1-Remember; K2-Understand; K3-Apply; K4-Analyze; K5 -Evaluate; K6 -Create					
46)					
LIST OF PROGRAMS	45 hours				

OBJECT ORIENTED PROGRAMMING WITH C++ (Any 6)

- 1. Create a class to implement the data structure STACK. Write a constructor to initialize the TOP of the stack to 0. Write a member function POP() to delete an element. Check for overflow and underflow conditions.
- 2. Create a class ARITH which consists of a FLOAT and an integer Variable. Write member ADD(), SUB(), MUL(), DIV(), MOD() to perform addition, multiplication, division and modulus respectively. Write member functions to get and display values.
- 3. Create a class MAT has a 2-d matrix and R&C represents the rows and columns of the matrix. Overload the operators +, -, * to add subtract and multiply two matrices. Write member functions to get and display MAT object values.
- 4. Create a class STRING. Write member function to initialize, get and display strings. Overload the operator + to concatenate two strings, = = to compare two strings and a member function to find the length of the string.
- 5. Create a class which consists of EMPLOYEE detail like eno, ename, dept, basic-salary, grade. Write member functions to get and display them. Derive a class PAY from the above class and write a member function to calculate da, hra, pf depending on the grade and Display the Payslip in a neat format using console I/O.

- 6. Create a class SHAPE which consist of two VIRTUAL FUNCTIONS Cal_Area() and Cal_PERI to calculate AREA and PERIMETER of various figures. Derive three classes SQUARE,RECTANGLE and TRIANGLE from the class SHAPE and calculate AREA and PERIMETER of each class separately and Display the result.
- 7. Create two classes which consists of two private variables, one float And one integer variables in each class. Write member functions to get and display them. Write FRIEND function common to arguments. And the integer and float values of both the objects separately and Display the result.
- 8. Write a user defined function USERFUN() which has the formatting commands like setw(), showpoint, showpos precision(). Write a program which prints an multiplication table and uses USERFUN() for formatting.
- 9. Write a program to perform Insertion, Deletion and Updation using files.
- 10. Write a program which takes a file as argument and copies in to another file with line numbers using Command Line Arguments.

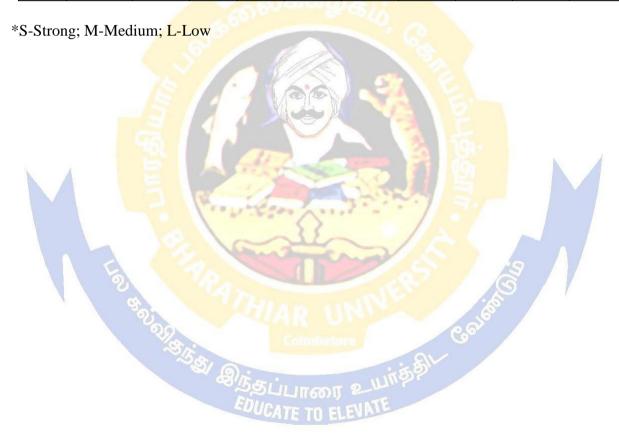
PYTHON PROGRAMMING LAB (Any 6)

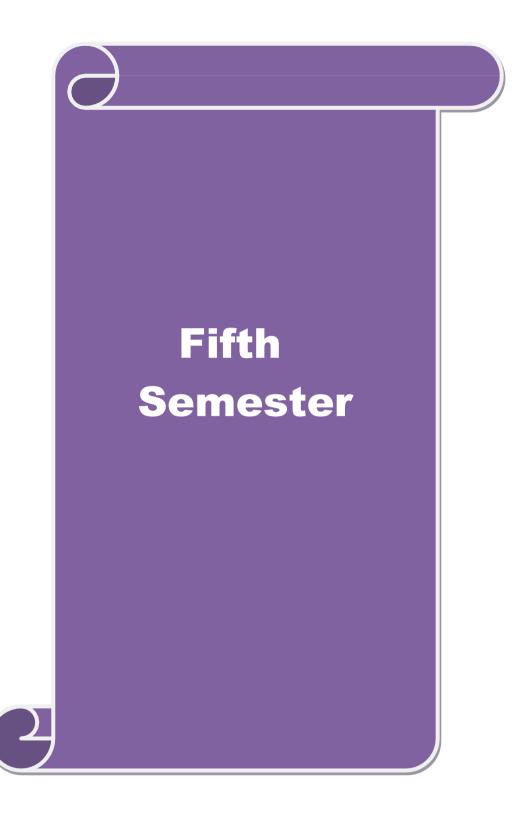
Implement the following in Python:

- 1. Write a Python program by getting input from the user and displays the following information: Your name, Full address, Mobile number, College name, Course, Subjects.
- 2. Write a Python program to find the largest three integers using if-else and conditional operator.
- 3. Write a Python program that asks the user to enter a series of positive numbers (The user should enter a negative number to signal the end of the series) and the program should display the numbers in order and their sum.
- **4.** Write a Python program to find the product of two matrices [A] m x p and [B] p x r.
- 5. Write recursive functions for Fibonacci sequence up to given number n.
- **6.** Write recursive functions to display prime number from 2 to n.
- 7. Write a Python program that writes a series of random numbers to a file from 1 to n and display.
- **8.** Write a Python program to sort a given sequence: String, List and Tuple.
- **9.** Write a Python program to make a simple calculator using functions.
- **10.** Write a Python program for Linear Search using functions.
- 11. Write a Python program for Inheritance
- 12. Write a Python program to slice a given list.
- 13. Write a Python program to count the number of words.
- 14. Write a Python program to copy a file.
- **15.** Write a Python program to check the given password is correct or not using regular expressions.

	Total Lecture hours 45 hours
7	Payt Dooles
	Cext Books
1	Bill Lubanovic, "Introducing Python", O'Reilly, First Edition - Second Release, 2014.
2	Mark Lutz, "Learning Python", O' Reilly, Fifth Edition, 2013.
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	Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur &
	Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	Mapping with Programme Outcomes									
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	S	M	M	S
CO2	S	M	M	M	M	M	S	L	L	L
CO3	S	S	S	M	M	L	L	L	M	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	M	L	M	M	S	M





The main ob	ectives: jectives of this co	urse are to:				
Pre-rec		Digital Electronics8085 Microprocessor	Syllabus Version		202	5-26
Core /El Suppo		Core paper VI	5	Т		4
Course code	53A	8051 MICROCONTROLLER AND ITS APPLICATIONS	L	Т	P	C

- ❖ To Study the architecture and addressing modesof8051
- ❖ To Impart knowledge about assembly language programsof8051
- to understand the importance of different peripheral devices and their interfacing to 8051
- ❖ To Impart knowledge of different types of external interfaces including LCD, Keypad Matrix, Stepper motor and sensors

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

1	Describe architecture and operation of Microcontroller 8051	K1
2	Foster ability to understand the design concept of interfacing Microcontroller with	K2
	various per <mark>ipherals</mark>	
3	Analyze the data transfer and interfacing techniques	/ K3
4	Foster ability to understand the role of embedded systems in industry	K4
5	Analyze the data transfer through serial and parallel ports.	K5

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

Unit:1 MICROCONTROLLER OVERVIEW 18 hours

Introduction to Microcontroller — 8051 Microcontroller Block Diagram - 8051 Oscillator and Clock — A, B & Register Banks — Stack - Program Counter & Data Pointer— — Flag & PSW — Special Function Registers — Internal Memory — Input / Output Pins & Ports.

Unit:2	ADDRESSING MODES, ARITHMETIC AND LOGICAL	18
	INSTRUCTION	hours

Introduction – Addressing Modes: Direct – Indirect – Register - Indexed – Arithmetic Instructions: Addition, Subtraction, Multiplication, Division, Increment And Decrement – Logic & Compare Instructions: AND, OR, XOR, CPL & Compare – Rotate & Swap Instruction: RR, RL, RRC, RLC.

Unit:3	DATA TRANSFER AND BRANCH OPERATIONS	18
		hours

Introduction – Internal Data Move - External Data Move – Code Memory Read Only Data Move - Loop and Jump Instructions – Conditional Jump – Unconditional Jump – Call Instructions: LCALL and ACALL – Push and Pop Instructions – Simple Programs.

Unit:4 PERIPHERALS 18hours

Timer and Counter: Timer Registers – TMOD – TCON - Mode 1 and 2 Programming – Counter Programming – Serial: MAX232 – Baud Rate – SBUF Register – SCON Register – Program to Transfer and Receive Data Serially.

Unit:5	INTERFACING	18hours
	ing of LCD - LCD Operation - Pin Descriptions - Command and Data t	to the LCD –
ADC 0804	– LM34 Temperature Sensor – Stepper Motor – Interfacing Key Board.	
		loo -
	Total Lecture hours	90 hours
Text Bo	ok(s)	
1 Mohan	ned Ali Maszidi & Janice Gillispie Maszidi, "The 8051 Microcontroller	and
Embed	ded System", Pearson Publishers	
2 Kennet	h J. Ayala, "The 8051 Microcontroller Architecture, Programming a	nd
Applic	ation"2 nd Edition, Penram Int ernational Publications	
Referen	ce Books	
1 Myke 1	oredko, "Programming and Customizing the 8051 Microcontroller", '	Tata McGraw
	Edition	
2 AyalaJ	K., —The8051 Microcontroller:Architecture, programming and app	olications",
Penran	International (2005) 3rd edition.	
Related	Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]	
1 https://	www.youtube.com/watch?v=84YUQu8tE4w	
2 https://	www.youtube.com/watch?v=GPz/mR7Flas	
3 https://	www.youtube.com/watch?v=uFhDGagZzjs	
_	19L (2)	
	G D : 1D	

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode..

Mappi	ng with P	rogra <mark>mn</mark>	ne Outo	comes	3	Eng			-	A
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	S	M	M	M	S	Lie	L	S
CO2	S	S	S	M	M	M	Soll	L	L	L
CO3	S	S	S	M	LIMTE TO	OIT B-U ELEVAT	L	S	S	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	S	L	M	M	S	S

^{*}S-Strong; M-Medium; L-Low

Course Code	53B	INTERNET OF THINGS	L	T	P	C	
Core/Elective		CORE PAPER VII	5	Т		4	
/Supportive: Pre-			Cvilla	hua	2025.26		
Pre- Requisite:		Basic Electronics	Sylla Vers			2025-26	
Course Object	tives:						
 The Main Obj	ectives o	f this course are to:					
 To enab 	ole the stu	idents to learn about IoT and also to und vices and Inter <mark>facing sensors.</mark>	erstand th	e cor	ncep	t	
Expected Cou	rse Outc	omes:					
On the Success	ful comp	letion of the course, student will be able	to:				
1 Study th	ne conc <mark>e</mark> p	ot of basic IoT				K1	
2 Familia	rize t <mark>he p</mark>	rinciple of connected devices	2 (K2	
		about embedded devices	E			К3	
4 Analyze		t sensor Interface technology	19.			K4	
	K4						
		applications					
5 Analyze K1 :Remember		understand; K3 -Apply; K 4-Analyze;	K5-Eval	uate;]	K6-Create	
K1:Remembe		Understand; K3 -Apply; K 4-Analyze;	K5-Eval	uate;		K6-Create	
K1:Remembe Unit:1 Introduction t	o IoT: Ev	Understand; K3-Apply; K4-Analyze; IOT FUNDAMENTALS volution of IoT – Definition & Character	ristics of	IoT -	Arc	K6-Create 18 hours hitecture o	
K1:Remembe Unit:1 Introduction t	o IoT: Ev	Understand; K3-Apply; K4-Analyze; IOT FUNDAMENTALS	ristics of I	IoT - rity i	Arc n Io	K6-Create 18 hours hitecture o	
K1:Remembe Unit:1 Introduction t IoT – Techno Unit:2	o IoT: Evlogies for	Understand; K3-Apply; K4-Analyze; IOT FUNDAMENTALS volution of IoT – Definition & Character IoT – Applications of IoT – Industrial Io DESIGN PRINCIPLES FOR CON	ristics of lot Port - Secu	IoT - rity i	Arc n Io'	K6-Create 18 hours hitecture of Γ hour s	
K1:Remembe Unit:1 Introduction t IoT – Techno Unit:2	o IoT: Evlogies for	Understand; K3-Apply; K4-Analyze; IOT FUNDAMENTALS volution of IoT – Definition & Character r IoT – Applications of IoT – Industrial Io DESIGN PRINCIPLES FOR CON DEVICES systems - Communication Technologies	ristics of DOT – Secu NECTEI s - Data m	IoT - rity i	Arc n Io'	K6-Create 18 hours hitecture of Γ hour s	
K1:Remember Unit:1 Introduction to IoT – Techno Unit:2 Introduction-loonsolidation Unit:3 Arduino IDE	o IoT: Evologies for And Devi	IOT FUNDAMENTALS volution of IoT – Definition & Character of IoT – Applications of IoT – Industrial Io DESIGN PRINCIPLES FOR CON DEVICES systems - Communication Technologies ice management - Ease of Designing and GRAMMING FUNDAMENTALS WIT	ristics of DOT – Secu NECTEI s - Data m l Affordal TH C	IoT - rity i nanagoility	Arcc 18 18 18 18 18 18 18 18 18 18 18 18 18	K6-Create 18 hours hitecture of Γ hour s ent, data 8 hours Conditiona	
Unit:1 Introduction to IoT – Techno Unit:2 Introduction-loonsolidation Unit:3 Arduino IDE Statements an	o IoT: Evologies for And Devi	IOT FUNDAMENTALS volution of IoT – Definition & Character of IoT – Applications of IoT – Industrial Io DESIGN PRINCIPLES FOR CON DEVICES systems - Communication Technologies ice management - Ease of Designing and GRAMMING FUNDAMENTALS WIT USING ARDUINO IDE Syntax – Data Types/ Variables/ Consta	ristics of DOT – Secu NECTEI s - Data m l Affordal TH C	IoT - rity i nanagoility	Arc n Io' 18 18 18 19 19 19 19 19 19 19	K6-Create 18 hours hitecture of Γ hour s ent, data 8 hours Conditiona	
Unit:1 Introduction to IoT – Techno Unit:2 Introduction-loconsolidation Unit:3 Arduino IDE Statements and invoking Function	o IoT: Evelogies for PROCO Basic Send Loops ections.	IOT FUNDAMENTALS volution of IoT – Definition & Character r IoT – Applications of IoT – Industrial Io DESIGN PRINCIPLES FOR CON DEVICES systems - Communication Technologies ice management - Ease of Designing and GRAMMING FUNDAMENTALS WIT USING ARDUINO IDE Syntax – Data Types/ Variables/ Consta	ristics of DoT – Secu NECTEI s - Data mand Affordata TH Cant – Opens for Serultrasound	IoT - rity i nanagoility erator ial, c	Arc n Io' 18 gemee 1 11 11 11 11 11 11 11 11	18 hours hitecture of Γ hour s ent, data 8 hours Conditiona 7 and othe	
Unit:1 Introduction to IoT – Techno Unit:2 Introduction-loonsolidation Unit:3 Arduino IDE Statements and invoking Function Function Function IDE Introduction IDE Statements and Invoking Function IDE Invoking IDE Invoki	To IoT: Evelogies for PROCO — Basic and Loops ctions. Digital Serith Arduit	IOT FUNDAMENTALS volution of IoT – Definition & Character of IoT – Applications of IoT – Industrial Io DESIGN PRINCIPLES FOR CON DEVICES systems - Communication Technologies ice management - Ease of Designing and GRAMMING FUNDAMENTALS WIT USING ARDUINO IDE Syntax – Data Types/ Variables/ Constate is – Using Arduino C Library Function SENSORS AND ACTUATORS insors – Interfacing temperature sensor, u	ristics of DoT – Secu NECTEI s - Data mand Affordate TH C ant – Opens for Serultrasound Arduino.	IoT - rity i nanagoility erator ial, c	Arcon Io' 18 18 19 19 10 10 10 10 10 10 10 10	18 hours hitecture of Γ hour s ent, data 8 hours Conditiona 7 and othe	
Unit:1 Introduction to IoT – Techno Unit:2 Introduction-loconsolidation Unit:3 Arduino IDE Statements and invoking Function Function of IDE Introduction to IDE Introduction to Introduction Introducti	o IoT: Evelogies for PROCO Basic Send Loops ctions. Original Senith Arduit SENITO ESP820	IOT FUNDAMENTALS volution of IoT – Definition & Character of IoT – Applications of IoT – Industrial Io DESIGN PRINCIPLES FOR CON DEVICES systems - Communication Technologies ice management - Ease of Designing and GRAMMING FUNDAMENTALS WIT USING ARDUINO IDE Syntax – Data Types/ Variables/ Constate S – Using Arduino C Library Function SENSORS AND ACTUATORS asors – Interfacing temperature sensor, uno – Interfacing LED and Buzzer with A	ristics of DoT – Secu NECTEI s - Data mand Affordate TH C ant – Opens for Serultrasound Arduino. NET mming NO	nanagoility eratorial, o	Arc n Io' 18 gemee 1 1 or an	18 hours hitecture of Γ hour s ent, data 8 hours Conditiona 7 and othe 8 hours nd infrared 18 hours using	

Text 1	Book(s)
1	Arshdeep Bahga, Vijay Madisetti, —Internet of Things: A Hands-On Approach", 2014. ISBN: 978-0996025515
2	Boris Adryan, Dominik Obermaier, Paul Fremantle, — The Technical Foundations Of Iot ", Artech Houser Publishers, 2017.
Refer	ence Books
1	Michael Margolis,—Arduino Cookbook", O"Reilly, 2011
2	Marco Schwartz, —Internet of ThingswithESP8266", Packt Publishing,2016
Relat	ed Online Contents [MOOC, SWAYAM, NPEL, Website etc.]
1	https://nptel.ac.in/courses/106/105/106105166/Introduction to IoT Part I – Lecture 1
2	-https://ocw.cs.pub.ro/courses/iot/courses/02Electronics for Internet of Things – Lecture II
3	https://nptel.ac.in/courses/106105166/Introduction to Arduino – I – Lecture 22
	Course Designed By:
Dr.	Archana, Assistant professor, Nehru Arts and Science College, Coimbatore.& N Om Muruga, Assistant Professor, Government Arts College Ooty, N Omprakash Anand, Assistant Professor, Government Arts College, Ooty.

Mappi	ng with <mark>P</mark> i	<mark>rogram</mark> n	ne Outo	comes			2	(A) -		9
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	S	M	M	S
CO2	S	M	M	M	M	M	S	L	L	L
CO3	S	S	S	M	M	L	L	L ©	M	M
CO4	M	M	M	S	S	S	L	9	M	M
CO5	M	M	S S	S	M	L	5 M	M	S	M

^{*}S-Strong; M-Medium; L-Low

Course code	5ZC	VISUAL PROGRAMMING	L	T	' P	C			
Core /Elective Supportive:	/	Skill Based Subject III:	3	Т		3			
Pre-requisite		Computer skills and familiarity with Microsoft Windows.							
Course Objecti	ives:								
The main object	tives of thi	s course are to:							
To design	n and devel	lop Windows-based applications using Visual Basic							
		fundamentals of design, development, implementati	on						
	mentation								
To Gain l	knowledge	about to write visualC++programming							
Expected Cour	se Outc <mark>on</mark>	nes:							
On the success	sful co <mark>mple</mark>	etion of the course, student will be able to:							
1 Explore Vi	sual <mark>Basic</mark>	°s .			K	.1			
2 Implement	syntax rul	es in Visual Basic programs			K	2			
3 Explain var	riable <mark>s and</mark>	data types used in program development			K	[3			
4 Write and a	a <mark>pply visua</mark>	al C++ principles and programming techniques			K	[4			
5 Write and a	apply proce	edures, sub-procedures, and functions to data transfer	r	k	K	5			
K1 - Remembe	er; K2 - U1	nderstand; K3 - Apply; K4 - Analyze; K5 - Evaluate	; K6 –	- Crea	te				
Unit:1	1 19	VISUAL BASIC OVERVIEW			9ho	ours			
Window Messa	ige – Dev	Programming – Event Driven Programming – Data vice Context – Document Interfaces – Dynamic ools – Context Help.							
Unit:2	9	VISUAL BASIC OBJECTS		7	9ho	ours			
MenusandToolb	ars-Using	g – Forum Design – VBX Controls – Properties – Eve DialogBoxes–WorkingwithControlArrays–ActiveXC Face (MDI) – F <mark>ile System Con</mark> trols – Data Control – I	Contro	ls	res				
Unit:3		BUILDING THE USER INTERFACE			9ho	ours			
Handling – Mes	sage Dispa Architecti	g – Frame Work Classes – VC++ Components - atch System – Model and Modeless Dialogs – Impo- ure – Serialization – Multiple Document Interface - atrols	rtant \	VBX (Contr	ols –			
Unit:4		DATABASE AND APPLICATIONS			9 h	ours			
		Min Database Applications – Embedding Controls i							
		Based Applications – Dynamic Data Transfer For – Communicating with other applications – ObjectLi			Data	Base			
Unit:5		DESIGN AND DEVELOPMENT			9 h	ours			
	_	isual Interface Design – File System – Storage and R orm Development	etrieva	al Sys					
		Total Lecture ho	urs	45 ho	urs				

1	Text Book(s)
1	Petzold, Windows Programming, Microsoft Press, 1995
2	Marion Cottingham, Visual Basic , Peachpitt Press, 1999
3	Kate Gregory, Using Visual C++I, Prentice Hall of India Pvt. Ltd
4	PapparandMurray, VisualC++: The Complete Reference , Tata McGraw Hill, New Delhi, 2000.
E	Pafaranca Rooks

Reference Books

- 1 John Paul Muller, "Visual C++ from the Ground Up", Tata McGraw Hill, Delhi.
- 2 Richard C. Leinecker and Tom Archer, "Visual C++ 6 Programming Bible", Wiley India Pvt Ltd.

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

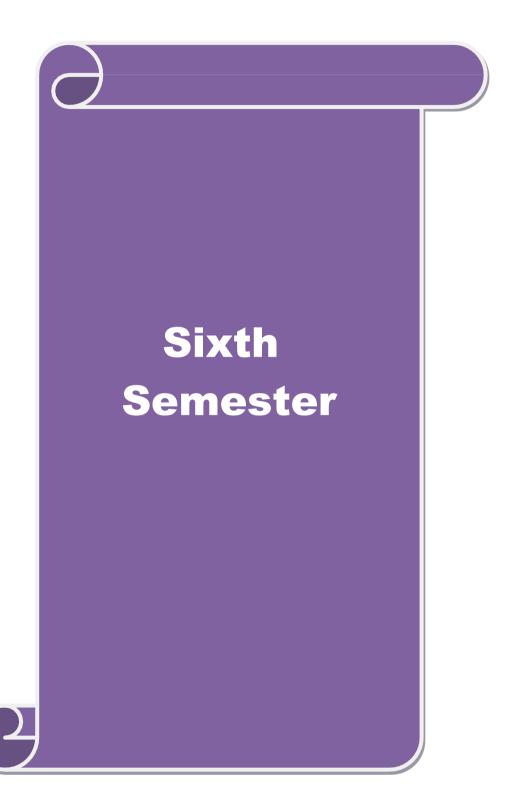
- 1 https://www.youtube.com/watch?v=5nahqfJTQXs
- 2 https://www.youtube.com/watch?v=1oGpl6qNKoQ
- 3 https://www.youtube.com/watch?v=gcFHyVYdeFU

Course Designed By:

Dr.K. Venmathi, Assistant professor, L.R.G. Govt Arts College for Women, Tirupur & Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappin	Mapping with Programme Outcomes												
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10			
A			1.00	1.6.0	A V	, 6. =				1			
CO1	S	S	S	M	M	M	S	L	L	S			
		AL.	N	me		1							
CO2	S	S	S	M	M	M	S	L	S L	L			
- 7	8		100			101		S. S.					
CO3	S	S	S	M	M	L	L	S	S	M			
		000		-				୍ ଚ					
CO4	M	M	M	S	S	S	L	L	M	M			
			\$ 6			0	55						
CO5	M	M	S	5S	S	· PIIII	M	M	S	S			
				EDUCA	TE TO E	EVATE							

^{*}S-Strong; M-Medium; L-Low



Course code	63A	DESIGN WITH PIC MICROCONTROLLER	L	T	P	C	
Core/Elect		Core paper VIII	6			4	
Pre-requi		Students having knowledge of electronics fundamentals coupled with some programming experiences are the ideal participants for this Course.	ramming Syllabus				
Course Object	ives:			I			
The main object	tives of	this course are to:					
•		hitecture and addressing modes of PIC Microcontroller					
		ge about assembl <mark>y language prog</mark> rams of PIC Microcontro					
To know	various	s perip <mark>heral devices and to interface them with</mark> PIC Microc	control	ler			
Expected Cou	rse Out	comes:					
On the success	sful con	npletion of the course, student will be able to:					
1 Understar	nd the e	volution in microcontroller technology			K	ĺ	
2 Describe	Harvard	l architecture model and programming techniques			K.	2	
3 Describe	the oper	ration and need for interrupts and timers			K.	3	
		pperators in modelling the design units			K		
		ot for I/O port expansion			K:	 5	
A		- Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; F	₹6 – C	reate	1		
Unit:1		CPU ARCHITECTURE AND INSTRUCT			15h		
	vard Aı	chitecture and Pipelining – Program Memory Consideration					
		ng Modes – CPU registers – Instruction Set – Simple Oper					
Unit:2		LOOP TIME SUBROUTINE, TIMER 2 AND	7	7	15 h	ours	
	2	INTERRUPTS		1			
Timer 2 Use – I Routine – Loop		Logic – Timer 2 Scalar Initialization – Interrupt Service - ubroutine	Interr	upt S	ervic	e	
Unit:3		EXTERNAL INTERRUPTS AND TIME	ERS		15 h	ours	
	nal Inte	errupt Input – Timer 0 – Compare Mode – Capture N		- Tin			
		Scalar – Timer1 External Event Counter – Timer1 and					
Width Modulate	ed Outp	uts – Port B Change Interrupts	_				
Unit:4		I/O PORT EXPANSION			15h	ours	
Synchronous Se	rial Por	t Module – Serial Peripheral Interface – Output Port Expa	nsion -	- Inpı	ıt Po	rt	
Expansion – LC	D Disp	lay					
Unit:5		I ² C BUS FOR PERIPHERAL CHIP AC	CESS		15h	ours	
I2C Bus Operat	ion - I20	C Bus Subroutines – DAC Output – Temperature Sensor –	- Serial	EEP	ROM	1	
		Total Lecture hours			75 h	ours	
Text Book(s)							
1 John B.Peat	man,—]	Design with PIC Microcontrollers —,Pearson Education	Publis	hing			
D.f. D	-1						
Reference Bo	OKS						

Muhammad Ali Mazidi, Rolin D. McKinlay, and Danny Causey, "The PIC MicrocontrollerandEmbeddedsystems—UsingAssemblyandCforPIC18, Prentice Hall, 2007

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

- 1 https://www.youtube.com/watch?v=nLlBssKCN2w
- 2 https://www.voutube.com/watch?v=VEAYB1A9SiA
- 3 https://www.youtube.com/watch?v=aSsnLyKtIAU

Course Designed By:

Dr.K. Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

7.7	41 D	0								
Mapping wi	th Prograi	mme Out	comes							
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	S	M	M	M	S	L	L	S
CO2	S	S	S	M	M	M	S	L	L	L
CO3	S	S	S	M	M	L	L	S	S	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	S	L	M	M	S	S

*S-Strong; M-Medium; L-Low

A SECULITION 2 LLITE SECULITIES SELECTION OF SELECTION O

Course code	63R	8051 MICROCONTROLLER	1 MICROCONTROLLER L T						
		LAB							
Core/Ele Suppor		Core –Practical – VI	3		p	4			
Pre-rec	quisite	Digital Electronics and 8085 Microprocessor	Sylla Vers		202	5-26			

Course Objectives:

The main objectives of this course are to:

To introduces the assembly language programming of Microcontroller

To develop the student's Assembly language programming skills and gives practical training of interfacing the peripheral devices with the Microcontroller

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

1	Apply the fundamentals of assembly level programming of microcontroller	К3
2	Design and Develop program for real time interface	K6

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

8051/ PIC PROGRAMMING

(ANY 12 EXPERIMENTS)

- 1. Addition / Subtraction of 8 / 16bitData
- 2. Multiplication / division 8bitData
- 3. Block Data Transfer
- 4. Smallest / Largest of N Numbers
- 5. To Arrange in Ascending / Descending Order
- 6. Sum of N 8 bit Numbers
- 7. 1's and 2's Compliment of an Array (8/16bit)
- 8. UP/DOWN Counter using 7SegmentDisplay
- 9. Traffic Light Control Interface
- 10. Wave Form Generation
- 11. ADC Interface
- 12. DAC Interface
- 13. Stepper Motor Interface
- 14. Solid State Relay Interface
- 15. DC Motor Interface
- 16. Temperature Controller
- 17. Rolling and Blinking of a Message
- 18. LCD Interface
- 19. Frequency Counter
- 20. Water Level Indicator

Course Designed By:

Dr.K. Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & Mr.S. Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode..

Mappi	ng with P	rogramn	ne Outo	comes						
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	M	M	S	S	S	M	S	M	M	S
CO2	S	S	S	M	M	M	S	M	L	L
CO3	S	S	M	M	M	L	L	L	S	M
CO4	S	S	M	S	S	S	L	L	S	M
				1	- 					
CO5	M	M	S	S	S	L	L	L	S	M
							10			

*S-Strong; M-Medium; L-Low



Course code	63P	Internet of Things System Design Lab	L	Т	P	С
Core/Ele Suppo		3		P	4	
Pre-rec	quisite	Basic knowledge of Electronic Circuits or permission of	Syllah Versi		202	25-26
		instructor				

Course Objectives:

The main objectives of this course are to:

- ❖ To make the students to design IoT based circuits
- To understand the characteristics of Arduino & sensors.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

On	the successful completion of the course, student will be able to.	
1	Interfacing the system and IoT	K4
2	Understand the characteristics of Internet of Things.	K6
3	Design the smart works	K5
4	Design Various Applications using IoT	K5
5	Understand the Sensors methods	K5

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

(ANY 8 EXPERIMENTS)

- 1. Installation and Library Management
- 2. LED interfacing.
 - a. Blinking
 - b. Running Light
- 3. Switch Interfacing
 - a. Without Interrupt
 - b. With Interrupt
- 4. Serial Communication & Plotter
- 5. ADC Interfacing
- 6. Buzzer interfacing
- 7. Tone Generation
- 8. LCD interfacing
- 9. Servo Motor interfacing
- 10. Joystick interfacing
- 11. Tilt Sensor interfacing
- 12. Reed Switch interfacing
- 13. Thyristor protection circuit

Course Designed By:

Dr.T.Siva Kumar, Principal, RVS College Of Arts and Science, Coimbatore & Dr.K. Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur.

Mapping	g with Pro	gramme	Outco	mes						
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	M	M	S	S	S	M	S	M	M	S
CO2	S	S	S	M	M	M	S	M	L	L
CO3	S	S	M	M	M	L	L	L	S	M
CO4	S	S	M	S	S	S	L	L	S	M
CO5	M	M	S	S	S	L	L	L	S	M



Course code	63Q	INDUSTRIAL AND POWER ELECTRONICS	L	T	P	С
Core/Ele Suppor		Core –Practical- VIII	3		P	4
Pre-rec	quisite	Basic knowledge of Electronic Circuits or permission of instructor	Syllah Versi		202	25-26

Course Objectives:

The main objectives of this course are to:

- ❖ To make the students to design triggering circuits of SCR.
- To understand the characteristics of power electronic devices.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

1	Design triggering circuits of SCR	K4
2	Understand the characteristics of power electronic devices.	K6
3	Design power Inverter Circuits	K5
4	Design Various Applications of SCR	K5
5	Design Cyclo Converter Circuits	K5

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

Unit I INDUSTRIAL AND POWER ELECTRONICS 90 Hours

(ANY 12 EXPERIMENTS)

- 1. Triggering of SCR by R, C and Diac.
- 2. Design of snubber circuit.
- 3. Fan regulator using Triac.
- 4. Thyristor chopper.
- 5. TRIAC Flasher.
- 6. Commutation Techniques.
- 7. Speed control of DC motor using SCR.
- 8. Automatic street light controller
- 9. Burglar Alarm
- 10. Sequencer Circuit.
- 11. Power Inverter
- 12. Switching Regulators
- 13. Automatic Battery Charger
- 14. Fire alarm
- 15. ON / OFF relay control using opto coupler
- 16. Servo stabilizer
- 17. Layout and Art Work preparation for PCB
- 18. Etching Drilling and Component mounting of PCB
- 19. Temperature controller using AD 590/ LM 35.
- 20. Construction of Emergency Lamp.
- 21. Phase Control Circuit
- 22. Cyclo converter
- 23. Thyristor protection circuit

Course Designed By:

Dr.K. Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mapping	with Prog	ramme (Outcome	es						
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	M	M	S	S	S	M	S	M	M	S
CO2	S	S	S	M	M	M	S	M	L	L
CO3	S	S	M	M	M	L	L	L	S	M
CO4	S	S	M	S	S	S	L	L	S	M
CO5	M	M	S	S	S	L	L	L	S	M

*S-Strong; M-Medium; L-Low



Course code 6ZP	VISUAL PROGRAMMING LAB	L	T	P	C
Core/Elective/ Supportive	Skill Based Subject Practical-IV	3		P	2
Pre-requisite	Basic computer skills and familiarity with Microsoft Windows.	Sylla Vers		20	25-26

Course Objectives:

The main objectives of this course are to:

❖ Learn to design and develop Windows-based business applications using Visual Basic Emphasis on the fundamentals of structured design, development, implementation, and documentation

Expected Course Outcomes:

	vected Course Cateomer	
On	the successful completion of the course, student will be able to:	
1	Implement syntax rules in Visual Basic programs	K2
2	Explain variables and data types used in program development	K3
3	Write visual C++ principles and programming techniques	K6
4	Design Application Editor and Control	K5
5	Design VB application Link	K5

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

VIS<mark>UAL PROGRAMMING (ANY 8 EXPERIMENTS</mark>)

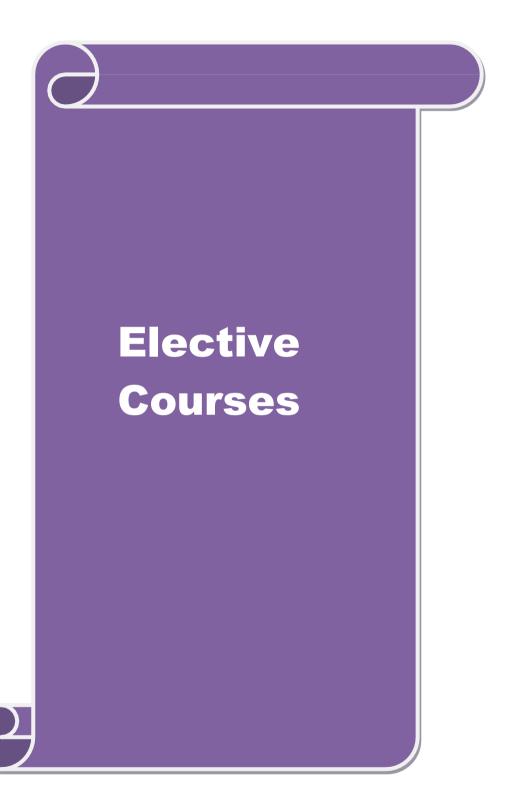
- 1. Building Simple Applications using Basic Tools
- 2. Working with Intrinsic Control and Active X controls
- 3. Create an Application with Multiple Forms and Dialogs
- 4. Write a VB Program to Design an e-mail Registration Form
- 5. Create an Application with Menu Editor
- 6. Create an Application with DAO Controls
- 7. Create an Application using Common Dialogs
- 8. Write a program for Drag and Drop Events
- 9. Create a Database for library management using ADD Controls
- 10. Creating an Application using Active X control
- 11. Create a Scientific Calculator in VB
- 12. Develop a VB application to either link or Embed MS Word Document to an OLE Control
- 13. Display Student Information using Grid Control
- 14. Create an Application using RDO Controls
- 15. Develop an Application to perform the following operation in the Employee Table using DAO
 - i) Add a new Record.
 - ii) Delete a Record.
 - iii) Modify a Record.

Course Designed By:

Dr.K. Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	ng with P	rogramn	ne Outo	comes						
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	M	M	S	S	S	M	S	M	M	S
CO2	S	S	S	M	M	M	S	M	L	L
CO3	S	S	M	M	M	L	L	L	S	M
CO4	S	S	M	S	S	S	L	L	S	M
CO5	M	M	S	S	S	L	L	L	S	M
					-					





Course	Code	5EA	PCB DESIGN AND FABRICATION	L	T	P)	C
Core/Elec Supporti			Elective-I-A	4	Т			3
Pre-re	quisite	function	knowledge of circuits familiar with the s and performance of various components I have a good logical thinking ability.		abus sion	2025-2	26	
Course Ob	jectives:	unc	inave a good rogical timixing atomey.			<u> </u>		
 To i To i To o To o Expected Con the Successions in the succession in the	mpart know earn how develop va Course Ou cessful con	he knowled whether to etch an arious tech atcomes:	of the course, student will be able to:				K1	
			ake use of the photo printing and etching tec	hnique	es		K2	
			n rules and automation techniques				К3	
4 U	Inderstand	d basic con	ncepts of transmission line, crosstalk and the	rmal is	ssues		K4	
A L	ircuits		d layout) PCB for analog circuits, digital circ				K5	
K1:Remer	noer, K	2-Unders	tand; K3-Apply; K4-Analyze; K5-Evalua TYPES OFPCB	ite;	N0-C	Create	18 h	
Single side Benefits of Resistors,	f Surface 1	Mount Te	ded – Multilayer boards – Plated through hole chnology (SMT) – Limitation of SMT – Surf Diode and IC's				ents:	
Unit:2		0	LAYOUT AND ARTWORK	0.6			18 h	ours
Spacing – and Packa	Supply an age Densit	nd Ground ty – Layo	yout of Layout – Resistance, Capacitance of Conductors – Component Placing and Mount Check-Basic Artwork Approaches – Awork Check and inspection	nting -	- Coo	ling Re	quirer	men
Unit:3		I	AMINATES AND PHOTO PRINTING				18 h	ours
Cleaning F	Process – I Resists – (Basic Prin Coating Pr	Laminates – Properties of Laminates – Types ting Process for Double Sided PCB's – Photococess for Wet Film Resists – Exposure and I	o Resi	sts –			₹ilm
Unit:4	-		ETCHING AND SOLDERING				18 h	

Unit:4 ETCHING AND SOLDERING 18 hours

Introduction – Etching Machine – Etchant System - Soldering: Principles of Solder Connection – Solder Joints – Solder Alloys – Soldering Fluxes - Soldering Tools: Soldering – De-soldering Tools and Techniques – Man Soldering – Solder Mask – Safety, Health and Medical Aspects in Soldering Practice

Unit:5	5 DESIGN RULES AND AUTOMATION						
	Reflection – Crosstalk – Ground and Supply Line Noise – Electromagnetic Interference from Pulse						
Type EM Fi	elds and Automation – Automated Artwork Drafting – CAD						
	Total Lecture hours	90					
		hours					

Text Boo	ok(s)
1	Walter C. Bosshart, —PCB Design and Technology, Tata McGraw Hill Publications, Delhi 1983
2	RS Khandpur, —Printed Circuit Board by Tata McGraw Hill Education Pvt Ltd., New Delhi
Reference	ze Books
1	S D Mehta, —ElectronicProduct Design Volume-I, S Chand Publications
Related (Online Contents [MOOC, SWAYAM, NPEL, Website etc.]
1	https://www.wikihow.com/Create-Printed-Circuit-Boards
2	http://www.siongboon.com/projects/2005-09-07_home_pcb_fabrication/
3	https://reprap.org/wiki/MakePCBInstructions#Making PCBs_yourself
4	https://www.youtube.com/watch?v=mv7Y0A9YeUc
4	https://www.youtube.com/watch?v=imQTCW1yWkg
	Course Designed By:
	Dr.K.Venmathi Assistant professor L.R.G.Govt Arts College for Women, Tirupur &

Dr.K.Venmathi ,Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	ng with P	rog <mark>ram</mark> n	ne Outo	comes	THE	and of				
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
		A						AA		11
CO1	S	o S	S	M	M	M	S	L	L	S
		29				-		್ಯಂ"	E.	
CO2	S	S	S	M	M	M	S	L	L	L
				19			55			
CO3	S	S	S	M	Mo	2 Lu	L	S	S	M
				EDUCA	TE TO E	EVATE				
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	S	L	M	M	S	S

^{*}S-Strong; M-Medium; L-Low

Course code 5EB	ADVANCED COMMUNICATION SYSTEMS	L	T	P	C
Core/ Elective/ Supportive:	Elective I B	4	Т		3
Pre-requisite	Principles of Communication Systems	Sylla Vers		2025	-26
Course Objectives:					
The main objectives of	f this course are to:				
To analyze various	ous data communication systems				
	dents understand the basic concept in the field of pulse co	mmuni	icatio	ns	
	nmunication systems				
	of satellite comm <mark>unication and expose the learners to the b</mark>	oasics o	f		
signal propagati	on through optical fibers				
Expected Course Out	tcomes:				
	npletion of the course, student will be able to:				
	ent types of Pulse communication systems			K	2
	ormance of modulation techniques			K	
, ,	43 11			K	
	Satellite and cellular communication system			K	
	rmance of cellular communication systems	V.		,	
1 1	le of optical fibre structure and its various applications			K	4
K1 - Remember K7	Inderestands K2 Applys K4 Applesta				
	- Understand; K3 - Apply; K4 - An4alyze; K5 - Evaluate	; K 0 - 0			
Unit:1 Introduction – Data Fo	DATA COMMUNICATION orms – Transmission Modes – Simplex, Half Duplex – Ful	ll Duple	18l ex – P	our oint	to
Unit:1 Introduction – Data Form Point Network – Star Modelicated Lines – Data	DATA COMMUNICATION	ll Duplo	18h ex – F al- up	our Point Lin	to es
Unit:1 Introduction – Data Form Point Network – Star Modelicated Lines – Data	DATA COMMUNICATION orms – Transmission Modes – Simplex, Half Duplex – Ful Network – Ring Network – Bus Network – Telephone Lin a Communication System – Asynchronous and Synchronous	ll Duplo	18h ex – F al- up nsmis	our Point Lin	to les -
Unit:1 ntroduction — Data Form Point Network — Star Modelicated Lines — Data JSART's and ACIA's Unit:2	DATA COMMUNICATION orms – Transmission Modes – Simplex, Half Duplex – Ful Network – Ring Network – Bus Network – Telephone Lin a Communication System – Asynchronous and Synchronous s – RS 232 Inter Connect Cable – Modems – Protocols	ll Duplo les – Di ous Tra	18hex — Fall al- up	our Point Lin Ssion	to nes n –
Unit:1 ntroduction – Data For Point Network – Star Not Dedicated Lines – Data JSART's and ACIA's Unit:2 Basic Concepts of Puls	DATA COMMUNICATION orms - Transmission Modes - Simplex, Half Duplex - Ful Network - Ring Network - Bus Network - Telephone Lin a Communication System - Asynchronous and Synchronous s - RS 232 Inter Connect Cable - Modems - Protocols PULSE COMMUNICATION	ll Duplo les – Di lous Tra	18h ex - F eal- up nsmis 18h M - P	Point Coint Lin Ssion hour	to nes- n –
Unit:1 ntroduction — Data For Point Network — Star Noedicated Lines — Data JSART's and ACIA's Unit:2 Basic Concepts of Pulse — Differential PCM— Dunit:3	DATA COMMUNICATION orms – Transmission Modes – Simplex, Half Duplex – Ful Network – Ring Network – Bus Network – Telephone Lin a Communication System – Asynchronous and Synchronous s – RS 232 Inter Connect Cable – Modems – Protocols PULSE COMMUNICATION se Modulation – Sampling Theorem – PAM – PTM – PFN Delta Modulation–Adaptive Delta Modulation–TDM– FDI SATELLITE CONMMUNICATION	ll Duplo les – Di lous Tra M – PPI M– AS	18hex – For al- up nsmiss 18h M – P K–FS 18 h	Point Coint	to nes- n – rs
Unit:1 ntroduction – Data Form Point Network – Star Modicated Lines – Data JSART's and ACIA's Unit:2 Basic Concepts of Pulsipoint PCM – Differential PCM – Dunit:3 Introduction – Satellite	DATA COMMUNICATION orms – Transmission Modes – Simplex, Half Duplex – Ful Network – Ring Network – Bus Network – Telephone Lin a Communication System – Asynchronous and Synchronous s – RS 232 Inter Connect Cable – Modems – Protocols PULSE COMMUNICATION se Modulation – Sampling Theorem – PAM – PTM – PFM Delta Modulation–Adaptive Delta Modulation–TDM– FDI	ll Duplo les – Di lous Tra M – PPI M– AS	18hex – For al- up nsmiss 18h M – P K–FS 18 h	Point Coint	to nes- n – rs
Unit:1 Introduction – Data For Point Network – Star Modelicated Lines – Data USART's and ACIA's Unit:2 Basic Concepts of Pulse – Differential PCM – Dunit:3 Introduction – Satellite Satellite Frequencies	DATA COMMUNICATION orms – Transmission Modes – Simplex, Half Duplex – Ful Network – Ring Network – Bus Network – Telephone Lin a Communication System – Asynchronous and Synchronous s – RS 232 Inter Connect Cable – Modems – Protocols PULSE COMMUNICATION se Modulation – Sampling Theorem – PAM – PTM – PFM Delta Modulation–Adaptive Delta Modulation–TDM– FDI SATELLITE CONMMUNICATION e Orbit – Satellite Position – Up link – Down Link – Cross	ll Duplo les – Di ous Tra M – PP M– AS	18h ex - F al- up nsmis 18h M - P K-FS 18 h - Assi	Point of Lingssion	rs PSE
Unit:1 ntroduction – Data For Point Network – Star Not Dedicated Lines – Data JSART's and ACIA's Unit:2 Basic Concepts of Pulse-Differential PCM – Eurit:3 Introduction – Satellite Satellite Frequencies Inside Satellite: Transport	DATA COMMUNICATION orms - Transmission Modes - Simplex, Half Duplex - Ful Network - Ring Network - Bus Network - Telephone Lin a Communication System - Asynchronous and Synchronous a - RS 232 Inter Connect Cable - Modems - Protocols PULSE COMMUNICATION se Modulation - Sampling Theorem - PAM - PTM - PFN Delta Modulation-Adaptive Delta Modulation-TDM- FDI SATELLITE CONMMUNICATION e Orbit - Satellite Position - Up link - Down Link - Cross ponder - Antenna System - Power Package and Station K	Il Duplo les – Di lous Tra M – PPI M– AS	18h ex - F al- up nsmis 18h M - P K-FS 18 h - Assi	Point of Lingssion	rs PSI rs ble
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Total Lecture hours

90hours

Text Book(s)	
1 Robert J. Shoernbeck,—Electronic Communications Modulation and Trans PHI,1999	smission∥,
2 . AnokSingh,—Principles of Communication Engineering, S.Chandand Co	ompany, 2 nd
Edition	
3 Sanjeev Gupta,—Electronic Communication System, Kanna Publishing Co	ompany
Reference Books	
1 Data communications and networking (sie) By behrouz a. Forouzan (author))
2 Electronic Communications 4th Edition by Dennis Roddy and J Coolen, Pear	rson Education
○ 5 5 5 10 5 1	
Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]	
1 https://nptel.ac.in/courses/108/101/108101113/	
2 https://nptel.ac.in/courses/117/105/117105143/	
3 https://nptel.ac.in/courses/106/106/106106167/	
Course Designed By:	
Dr.K.Venmathi "Assistant professor, L.R.G.Govt Arts College for Women, Tir Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.	rupur &

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^{*}S-Strong; M-Medium; L-Low

	de 5E0	\mathbb{C}	Introduction of Artificial Intelligence	L	Т	P	C		
Core/Electi	ve			4	Т		3		
/Supportiv	e:		Elective I -C						
Pre-			New Technology Introduction		abus		2025-26		
Requisite				Ver	sion				
Course Obj	jectives:								
The Main (Objectives	of	this course are to:						
			s bio-potentials and working principles of medical i ents to learn about bio <mark>-potentials and</mark> medical instru						
<u> </u>		uu	potentials and medical mistra						
Expected C									
On the succe	essful comp	leti	ion of the course, student will be able to:						
1	Understa	nd t	the evolution in microcontroller technology	A 7			K2		
2			rvard architecture model and programming techni	ques			K2		
3	Describe t	he	operation and need for interrupts and timers	6			K3		
4	List out v	ari	ous operators in modeling the design units	LE			K2		
5	Apply the	cc	oncept for I/O port expansion	163	•		K4		
K1:Remen	nber; K Z	2-U	<mark>Ind</mark> ers <mark>tan</mark> d; K3 -Ap <mark>ply; K</mark> 4-Anal <mark>yze; K5-Eva</mark> lt	ıa <mark>te;</mark>	K6	-Crea	ate		
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Unit:1			Introduction				18 hours		
	duction – D	efii	Introduction nition – Future of Artificial Intelligence – Characterist	tics of	Intell	igent			
Introd			a land	tics of	Intell	igent			
Introd Intelligent Ag			nition – Future of Artificial Intelligence – Characterist in Solving Approach to Typical AI Problems.	tics of	Intell	igent	Agents – Typical		
Introd Intelligent Ag Unit:2	gents – Prol	oler	nition – Future of Artificial Intelligence – Characterist in Solving Approach to Typical AI Problems. Problem Solving Methods	ල	L'EST		Agents – Typical 18 hours		
Introduction Introduction Intelligent Age Unit:2 Problem Solve	gents – Prol	ds	nition – Future of Artificial Intelligence – Characterist in Solving Approach to Typical AI Problems. Problem Solving Methods – Search Strategies – Uninformed – Informed – He	euristic	s – L	ocal	Agents – Typical 18 hours Search Algorithms a		
Introd Intelligent Ag Unit:2 Problem Solv Optimization	gents – Prol	ds	nition – Future of Artificial Intelligence – Characterist in Solving Approach to Typical AI Problems. Problem Solving Methods – Search Strategies – Uninformed – Informed – He Searching with Partial Observations – Constrain	euristic nt Sat	s – L isfact	ocal ion H	Agents – Typical 18 hours Search Algorithms a Problems – Constra		
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Software Agents – Architecture for Intelligent Agents – Agent Communication – Negotiation and Bargaining – Argumentation among Agents – Trust and Reputation in Multi-agent Systems. AI Applications Unit:5 18 hours AI Applications – Language Models – Information Retrieval – Information Extraction – Natural Language Processing Machine Translation – Speech Recognition – Robot – Hardware – Perception – Planning - Moving. **Total Lecture hours** 90 Hours Text Book(s) S. Russell and P. Norvig, —Artificial Intelligence: A Modern Approach, Prentice Hall, Third 1 Edition, 2009. I. Bratko, - Prolog: Programming for Artificial Intelligence, Fourth Edition, Addison- Wesley 2 Educational Publishers Inc., 2011. Reference Books M. Tim Jones, - Artificial Intelligence: A Systems Approach (Computer Science), Jones and Bartlett 1 Publishers Inc.: First Edition, 2008. 2 Nils J. Nilsson, - The Quest for Artificial Intelligence, Cambridge University Press, 2009. Related Online Contents [MOOC, SWAYAM, NPEL, Website etc.] 1 https://www.youtube.com/watch?v=i2mZylgP1Fk 2 https://www.youtube.com/watch?v=4ldv98F7Zng 3 https://nptel.ac.in/courses/108/105/108105101/ 4 https://nptel.ac.in/courses/108/105/108105091/ Course Designed By: Dr.T.Siva Kumar, Principal, RVS College Of Arts and Science, Coimbatore & Dr.K. Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur.

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CO2	S	M	M	M	M	M	S	L	L	L
CO3	S	S	S	M	M	L	L	L	M	M
CO4	M	M	M	S	S	S	L	L	M	M

M

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CO₅

Mapping with Programme Outcomes

S

M

S

^{*}S-Strong; M-Medium; L-Low

2 Able to analyze the parallel computer model with instruction level parallelism. 3 Gain the knowledge on pipelining. 4 Understand the memory hierarchy in developing an advanced computer.			7.111111	ated Coneges Berni dated 07.07.202.					
Core/Elective/Supportive: ELECTIVE I-D 4 T Pre-requisite: Basic Computer Architecture Syllabus version Course Objectives The objectives of this course are: ♣ To provide knowledge on fundamentals of Advanced Computer design. ♣ To understand the concept of instruction level parallelism, pipelining and memory hierarchy associated with it. ♣ To enhance the knowledge on advanced processors. Expected Course Outcomes: On successful completion of the course, student will be able to: 1 Gain the knowledge on advanced computer design principles. 2 Able to analyze the parallel computer model with instruction level parallelism. 3 Gain the knowledge on pipelining: 4 Understand the memory hierarchy in developing an advanced computer. 5 Apply the multiprocessor concepts in advanced processors. K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Creat Unit: 1 PRINCIPLES OF COMPUTER DESIGN 18 Ho Introduction-Quantitative principles of computer design - Control Units-Hardwired and Micro Programmed Design Concept - Microprogramming - Bus architectures: Unit-bus and multi-bus architectures: Unit: 2 INSTRUCTION LEVEL PARALLELISM 18 Ho Classification of parallel computers-Multiprocessors and multi-computers - Multi-vector and SIM computers-Instruction Level Parallelism. Overcoming Data Hazards with Dynamic Scheduling-Reducing Branch Penalties with Dynamic - Hardware Support for Extracting More Parallelism. Unit: 3 PIPELINING 18 Ho Introduction-the Fundamentals of Caches-Reducing Cache Misses and Miss Penalty - Reducing Hirt Time-Main Memory-Virtual Memory-Issues in Memory Hierarchy design. Unit: 5 PRINCIPLES OF ADVANCED PROCESSOR 18 Ho Advanced processor technology, CISC Scalar Processors, RISC Scalar Processors, Superscalar Processors, VLIW Architectures, Vector and Symbolic processors William Stallings, —Computer Architecture and Organization, 3rdEdition, TataMe-GrawHill, 1993. DA-PattersonandJ. L. Hennessey,—Computerorganization and Architecture — Designing for Performancel William Sta		Course Code	5FD		L	Т	P	C	
Pre-requisite: Basic Computer Architecture Syllabus version 2025 Course Objectives: The objectives of this course are: ❖ To provide knowledge on fundamentals of Advanced Computer design. ❖ To understand the concept of instruction level parallelism, pipelining and memory hierarchy associated with it. ❖ To enhance the knowledge on advanced processors. Expected Course Outcomes: On successful completion of the course, student will be able to: 1									
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The objectives of this course are:		<u> </u>	ite:	Basic Computer Architecture	•			025-26	
♦ To provide knowledge on fundamentals of Advanced Computer design. ♦ To understand the concept of instruction level parallelism, pipelining and memory hierarchy associated with it. ♦ To enhance the knowledge on advanced processors. Expected Course Outcomes: On successful completion of the course, student will be able to: 1 Gain the knowledge on advanced computer design principles. Able to analyze the parallel computer model with instruction level parallelism. 3 Gain the knowledge on pipelining. 4 Understand the memory hierarchy in developing an advanced computer. 5 Apply the multiprocessor concepts in advanced processors. KI - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Creat Unit: 1 PRINCIPLES OF COMPUTER DESIGN 18 Ho Introduction-Quantitative principles of computer design - Control Units-Hardwired and Micro Programmed Design Concept - Microprogramming - Bus architectures: Uni-bus and multi-bus architectures. Unit: 2 INSTRUCTION LEVEL PARALLELISM 18 Ho Classification of parallel computers-Multiprocessors and multi-computers - Multi-vector and SIM computers-Instruction Level Parallelism - Overcoming Data Hazards with Dynamic Scheduling-Reducing Branch Penalties with Dynamic - Hardware Support for Extracting More Parallelism. 18 Ho Basic concepts - Data hazards - Instruction hazards - Influence on instru									
To understand the concept of instruction level parallelism, pipelining and memory hierarchy associated with it. ★ To enhance the knowledge on advanced processors. Expected Course Outcomes: On successful completion of the course, student will be able to: 1	Γhe	-							
hierarchy associated with it. ◆ To enhance the knowledge on advanced processors. Expected Course Outcomes: On successful completion of the course, student will be able to: 1 Gain the knowledge on advanced computer design principles. 2 Able to analyze the parallel computer model with instruction level parallelism. 3 Gain the knowledge on pipelining. 4 Understand the memory hierarchy in developing an advanced computer. 5 Apply the multiprocessor concepts in advanced processors. KI - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Creat Unit: 1 PRINCIPLES OF COMPUTER DESIGN Is the Introduction-Quantitative principles of computer design - Control Units-Hardwired and Micro Programmed Design Concept - Microprogramming - Bus architectures: Uni-bus and multi-bus architectures. Unit: 2 INSTRUCTION LEVEL PARALLELISM Is Ho Computers-Instruction Level Parallelism - Overcoming Data Hazards with Dynamic Scheduling-Reducing Branch Penalties with Dynamic - Hardware Support for Extracting More Parallelism. Unit: 3 PIPELINING Is Ho Considerations - Performance considerations - Exception handling. Unit: 4 MEMORY HIERARCHY Is Ho Introduction-the Fundamentals of Caches-Reducing Cache Misses and Miss Penalty - Reducing Hit Time-Main Memory-Virtual Memory-Issues in Memory Hierarchy design. Unit: 5 PRINCIPLES OF ADVANCED PROCESSOR Is Ho Advanced processor technology, CISC Scalar Processors, RISC Scalar Processors, Superscalar Processors, VLIW Architectures, Vector and Symbolic processors Total Lecture Hours 90 Ho Text Books 1 Kai Hwang,—Advanced computer architecturel, Tata Mc.Graw Hill Science/Engineering/Math 1st Edition, 1992. 2 D.A. PattersonandJ.L. Hennessey,—Computerorganizationanddesign, MorganKaufmann, ARM Edition, 2010. Reference Books 1 Hayes, J.P.,—Computer Architecture and Organizationand Architecture — Designing for Performancel William Stallings,—Computer Organization and Architecture — Designing for Performancel		_	_	-	_				
Expected Course Outcomes: On successful completion of the course, student will be able to: 1				nstruction level parallelism, pipelinir	ng and m	nemory			
Consuccessful completion of the course, student will be able to: Gain the knowledge on advanced computer design principles. Able to analyze the parallel computer model with instruction level parallelism. Gain the knowledge on pipelining. Understand the memory hierarchy in developing an advanced computer. Apply the multiprocessor concepts in advanced processors. K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Creat Unit: 1 PRINCIPLES OF COMPUTER DESIGN 18 Ho Introduction-Quantitative principles of computer design - Control Units-Hardwired and Micro Programmed Design Concept - Microprogramming - Bus architectures: Uni-bus and multi-bus architectures. Unit: 2 INSTRUCTION LEVEL PARALLELISM 18 Ho Classification of parallel computers-Multiprocessors and multi-computers - Multi-vector and SIM computers-Instruction Level Parallelism - Overcoming Data Hazards with Dynamic Scheduling-Reducing Branch Penalties with Dynamic - Hardware Support for Extracting More Parallelism. Unit: 3 PIPELINING 18 Ho Basic concepts - Data hazards - Instruction hazards - Influence on instruction sets - Data path an control considerations - Performance considerations - Exception handling. Unit: 4 MEMORY HIERARCHY 18 Ho Introduction- the Fundamentals of Caches-Reducing Cache Misses and Miss Penalty - Reducing Hit Time-Main Memory-Virtual Memory-Issues in Memory Hierarchy design. Unit: 5 PRINCIPLES OF ADVANCED PROCESSOR 18 Ho Advanced processor technology, CISC Scalar Processors, RISC Scalar Processors, Superscalar Processors, VLIW Architectures, Vector and Symbolic processors Total Lecture Hours 90 Ho Text Books		•		1 1					
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On successful completion of the course, student will be able to: 1		and Course Out							
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4 Understand the memory hierarchy in developing an advanced computer. 5 Apply the multiprocessor concepts in advanced processors. K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Creat Unit: 1 PRINCIPLES OF COMPUTER DESIGN 18 Ho Introduction-Quantitative principles of computer design - Control Units-Hardwired and Micro Programmed Design Concept - Microprogramming - Bus architectures: Uni-bus and multi-bus architectures. Unit: 2 INSTRUCTION LEVEL PARALLELISM 18 Ho Classification of parallel computers-Multiprocessors and multi-computers - Multi-vector and SIM computers-Instruction Level Parallelism - Overcoming Data Hazards with Dynamic Scheduling-Reducing Branch Penalties with Dynamic - Hardware Support for Extracting More Parallelism. Unit: 3 PIPELINING 18 Ho Basic concepts - Data hazards - Instruction hazards - Instruction sets - Data path an control considerations - Performance considerations - Exception handling. Unit: 4 MEMORY HIERARCHY 18 Ho Introduction- the Fundamentals of Caches-Reducing Cache Misses and Miss Penalty - Reducing Hit Time-Main Memory-Virtual Memory-Issues in Memory Hierarchy design. Unit: 5 PRINCIPLES OF ADVANCED PROCESSOR 18 Ho Advanced processor technology, CISC Scalar Processors, RISC Scalar Processors, Superscalar Processors, VLIW Architectures, Vector and Symbolic processors Total Lecture Hours 90 Ho Science/Engineering/Math 1st Edition, 1992. D.A. Pattersonand J. L. Hennessey, —Computer organization and design, Morgan Kaufmann, ARM Edition, 2010. Reference Books 1 Hayes, J.P., —Computer Architecture and Organization 1, 3rd Edition, TataMc-Graw Hill, 1998 William Stallings, —Computer Organization and Architecture — Designing for Performancel					anensn	1.		K2	
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Note				1	C1.			K3	
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Total Lecture Hours 90 Horext Books 1 Kai Hwang,—Advanced computer architecture, Tata Mc. Graw Hill Science/Engineering/Math 1st Edition, 1992. 2 D.A. Pattersonand J. L. Hennessey,—Computer organization and design, Morgan Kaufmann, ARM Edition, 2010. Reference Books 1 Hayes, J. P., —Computer Architecture and Organization, 3rd Edition, Tata Mc-Graw Hill, 1998. 2 William Stallings, —Computer Organization and Architecture — Designing for Performance.		-			cessors,	Supers	scalar		
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Kai Hwang,—Advanced computer architecture ,Tata Mc.Graw Hill Science/Engineering/Math 1st Edition, 1992. D.A.PattersonandJ.L.Hennessey,—Computerorganizationanddesign, MorganKaufmann, ARM Edition, 2010. Reference Books Hayes,J.P., —Computer Architecture and Organization , 3rdEdition, TataMc-GrawHill, 1998 William Stallings, —Computer Organization and Architecture — Designing for Performance				Total Lecti	ure Hou	irs	90 H	lours	
Science/Engineering/Math 1 st Edition, 1992. D.A.PattersonandJ.L.Hennessey,—Computerorganizationanddesign, MorganKaufmann, ARM Edition, 2010. Reference Books Hayes, J.P., —Computer Architecture and Organization , 3rdEdition, TataMc-GrawHill, 1998 William Stallings, —Computer Organization and Architecture — Designing for Performance	Тех								
ARM Edition, 2010. Reference Books 1 Hayes, J.P., —Computer Architecture and Organization II, 3rdEdition, TataMc-GrawHill, 1998 2 William Stallings, —Computer Organization and Architecture — Designing for Performance	1	Science/Engineeri	ing/Math 1 st Ed	ition, 1992.					
1 Hayes, J.P., —Computer Architecture and Organization, 3rdEdition, TataMc-GrawHill, 1998 2 William Stallings, —Computer Organization and Architecture – Designing for Performance	2		•	—Computerorganizationanddesign,	Morgan	Kaufma	ann,		
William Stallings, —Computer Organization and Architecture – Designing for Performance	Ref	erence Books							
William Stallings, —Computer Organization and Architecture – Designing for Performance	1	Hayes, J.P., —Cor	nputer Architec	eture and Organization, 3rdEdition,	ГataМс-	GrawH	Iill, 19	98.	
	2	William Stallings,	, —Computer C	Organization and Architecture – Desi					

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

- 1 https://nptel.ac.in/courses/106/103/106103206/
- 2 https://www.youtube.com/watch?v=v7iefsovo9M
- 3 https://www.youtube.com/watch?v=L9X7XXfHYdU&list=PLxCzCOWd7aiHMonh3G6QNKq53C6oNXGrX

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

				Mapping	g with Pro	ogram O	utcomes			
Cos	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	S	L	L	L	L	L	L	L	L	L
CO2	S	M	L	L	M	L	M	L	L	M
CO3	S	M	S	L	P	L	L	L	L	M
CO4	S	S	L	L	L	L	L	L	L	L
CO5	S	S	M	M	M	L	M	L	L	M

*S-Strong; M-Medium; L-Low

R UNIVE Combatore

EDUCATE TO ELEVATE

Course Code	5EE	Arduino and Sensors	L	T	P	C
Core/Elective/Su	pportive:	ELECTIVE II-E	4	T		3
Pre-requis	ite:	Basic Computer Architecture	•	labus rsion	202	25-26
Course Objectives:		,			l .	
The objectives of this						
•	_	ndamentals of Advanced Computer des	_			
		f instruction level parallelism, pipelinin	ig and n	nemory		
hierarchy assoc		on advanced processors.				
* 10 cimanee the	knowledge o	in advanced processors.				
Expected Course Out	tcomes:	2000 G. (1000)				
		ırse, student will be able to:				
1 Gain the knowled	lg <mark>e on advanc</mark>	ed computer design principles.				K1
		mputer model with instruction level par	allelism	۱.		K4
3 Gain the knowled						K2
		chy in developing an advanced compute	er.			K2
		epts in advanced processors.				K3
K1 – Remember; K Unit: 1	<mark>2 – Understa</mark>	and; K3 – Apply; K4 – Anal <mark>yze; K5 – Arduino and Sensors</mark>	Evalua	ite; K6	– Crea 18 H	
irst circuit-Updated Cir	rcu <mark>it-Our</mark> Firs	t Program-Comments- Gotchas				
			25		10.77	
irst circuit-Updated Cir Unit: 2	rcuit-Our Firs		S		18 H	ours
Unit: 2	Embedded		What is t	ruth(tru	<u> </u>	ours
Unit: 2 "Blinky" - IF St	Embedded	1"C":	What is t	ruth(tru	<u> </u>	ours
Unit: 2 "Blinky" - IF St	Embedded	L''C'': SE Statements- WHILE statements - V	What is t	ruth(tru	<u> </u>	ours
Unit: 2 "Blinky" - IF St	Embedded	L''C'': SE Statements- WHILE statements - V	What is t	ruth(tru	<u> </u>	
Unit: 2 "Blinky" - IF St Combinations - FOR sta	Embedded attements - EL attements - Ou	L''C'': LSE Statements- WHILE statements - Ver New Circuit - Introducing Arrays			e) - 18 H	ours
"Blinky" - IF St Combinations - FOR sta Unit: 3	Embedded atements - EL atements - Ou ttons- Potenti	L''C'': LSE Statements- WHILE statements - Var New Circuit - Introducing Arrays Input and Output: cometers - RGB LEDs- Sound Circuit	t - Sim	ple note	e) - 18 H e - Mi	ours usic
"Blinky" - IF St Combinations - FOR sta Unit: 3 Input - Pushbut Music with functions —	Embedded atements - EL atements - Ou ttons- Potenti - Making a di	I "C": SE Statements- WHILE statements - Var New Circuit - Introducing Arrays Input and Output: ometers - RGB LEDs- Sound Circuit gital thermometer - Serial Monitor - Monito	t - Sim	ple note	e) - 18 H e - Mi	ours usic
"Blinky" - IF St Combinations - FOR sta Unit: 3 Input - Pushbut Music with functions -	Embedded atements - EL atements - Ou ttons- Potenti - Making a di	L''C'': LSE Statements- WHILE statements - Var New Circuit - Introducing Arrays Input and Output: cometers - RGB LEDs- Sound Circuit	t - Sim	ple note	e) - 18 H e - Mi	ours usic
Unit: 2 "Blinky" - IF St Combinations - FOR sta Unit: 3 Input - Pushbut Music with functions - Hooking up the LCD -	Embedded atements - EL atements - Ou ttons- Potenti - Making a dig	I "C": SE Statements- WHILE statements - Var New Circuit - Introducing Arrays Input and Output: ometers - RGB LEDs- Sound Circuit gital thermometer - Serial Monitor - March 2 LCD - Bringing it all together	t - Sim	ple note	e) - 18 H e - M empera	ours usic ture
"Blinky" - IF St Combinations - FOR sta Unit: 3 Input - Pushbut Music with functions -	Embedded atements - EL atements - Ou ttons- Potenti - Making a di	I "C": SE Statements- WHILE statements - Var New Circuit - Introducing Arrays Input and Output: ometers - RGB LEDs- Sound Circuit gital thermometer - Serial Monitor - March 2 LCD - Bringing it all together	t - Sim	ple note	e) - 18 H e - Mi	ours usic ture
Unit: 2 "Blinky" - IF St Combinations - FOR sta Unit: 3 Input - Pushbut Music with functions - Hooking up the LCD - ' Unit: 4	Embedded atements - EL atements - Ou ttons- Potenti - Making a di Talking to the	I''C'': LSE Statements- WHILE statements - Var New Circuit - Introducing Arrays Input and Output: cometers - RGB LEDs- Sound Circuit gital thermometer - Serial Monitor - March 2 LCD - Bringing it all together	t - Sim	ple note	e) - 18 H e - M empera	ours usic ture
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Unit: 2 "Blinky" - IF St Combinations - FOR sta Unit: 3 Input - Pushbut Music with functions — Hooking up the LCD - ' Unit: 4	Embedded atements - EL atements - Ou ttons- Potenti - Making a di Talking to the Sensors-1	I''C'': LSE Statements- WHILE statements - Var New Circuit - Introducing Arrays Input and Output: Ometers - RGB LEDs- Sound Circuit gital thermometer - Serial Monitor - Market LCD - Bringing it all together LCD - Bringing it all together Characterists and Circuit an	t - Sim	ple note	e) - 18 H e - M empera	ours usic ture ours

	Total Lecture Hours	90 Hours
t Books		
"Introduction	to Arduino", Alan G. Smith, 2011, ISBN: 1463698348 and IS	SBN-13: 978-
1463698348.		
erence Books		
"Exploring Arduino:	Tools and Techniques for Engineering Wizardry" by Jeremy	Blum, ISBN-
10 1118549368, Pub	lisher Wiley 2013.	
1	"Introduction 1463698348. erence Books Exploring Arduino:	Books "Introduction to Arduino", Alan G. Smith, 2011, ISBN: 1463698348 and II 1463698348.

Rel	ated Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]
1	https://nptel.ac.in/courses/106/103/106103206/
2	https://www.youtube.com/watch?v=v7iefsovo9M
3	https://www.youtube.com/watch?v=L9X7XXfHYdU&list=PLxCzCOWd7aiHMonh3G6QNKq53C6oNXGrX
	Course Designed By:

Dr.T.Siva Kumar, Principal, RVS College Of Arts and Science, Coimbatore & Dr.K.Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur.

	Mapping with Program Outcomes										
Cos	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	
CO1	S	L	L	L	L	L	L	L	L	L	
CO2	S	M	L	L	M	L	Me	L	L	M	
CO3	S	M	SS	L	L	Ling.	SIL	L	L	M	
CO4	S	S	L	EDUCAT	E 10 Ele Tuedoù	VAL	L	L	L	L	
CO5	S	S	M	M	M	L	M	L	L	M	

^{*}S-Strong; M-Medium; L-Low

Cour	se Code	5EF	MICI	DOWANE A	ND FIBER OPTIC	L	Т	P	С		
Cour	se Coue	SET	MICI		NICATION	L	1	Г			
Core/	Elective/			Elective II		4	Т		3		
	ortive:			Diceive II	•						
	equisite:	R	asic Ele	etronics and	Principles of	es of Syllabus					
TIC K	equisite.			nmunication	-	•	sion	2025	20		
Course (Objectives	:			Systems	, c.	51011				
The Mai	n Objectiv	ves of this	course	are to:							
* T	o inculcate	the princi	iple of n	nicrowave the	ory and working of w	aveg	uides				
* T	o know the	e operation	and ap	plications of t	fibre optic communic	ation					
* T	o impart kı	nowledge o	on the w	vorking princi	iple of microwave am	plifie	rs and	l oscill	ators		
	d Course (ುಹಕ್ಕು,	Szá	-					
				course, studer	nt will be able to:						
1		tand variou			eguide <mark>and use of</mark> con	npone	ent	K1			
2	1 11	e and find		ions and limi	tations of microwave			K4			
3	Discrimin		ent Rada	rs, find applic	cations and use of its			K5			
4				f Fiber Optic	s and types		h	K2			
5				-	and in Network Des	ign		К3			
K1·Ren	nember;	K2-Under	rstand: I	3-Apply:	K4-Analyze; K5-Ev	aluate	. 1	K6 -Cre	ate		
Unit:1					MICROWAVES	araace	,	18 H			
	tion – Max				aw – Faradays Law -	- Gai	iss La				
Equation	ı – Types	of Wave	Guides	– TE and TI	M Modes – Propagat ular Wave Guide.						
Unit:2	90	M		VAVE AMPI	LIFIERS AND			18 H	ours		
Microwa	ave Tubes:				ulti cavity Klystron	– Re	eflex	Klystro	on –		
	g Wave				Vave Oscillator (BV						
Unit:3		N	MICRO'	WAVE DEV	ICES & RADAR			18 H	ours		
	ave Transis			THE TO EAST	As) Metal Semi-cond	uctor	FET				
				,	Principle – Applicati						
			•		n – Factors Affecting						
					ne Position Indicato	or) –	Mov	ing Ta	ırget		
	r (MTI) – F	FM CW Ra		oplications.							
Unit:4				PTICAL FI				18 H	ours		
			\mathbf{C}	OMMUNIC	ATIO						
				N							
Basic Fi	ber Optic	System -	- Freque	encies – Fibe	r Optic Cables – Re	fracti	on –	Nume	rical		
A 4	O 1 1	T 1 0 1	1 0.	1 3 (1	3 f 1 2 3 f 1 C 1 1	\sim	. •	_			

Basic Fiber Optic System – Frequencies – Fiber Optic Cables – Refraction – Numerical Aperture – Graded Index Cables – Single Mode – Multi Mode – Cable Constructions – Cable Losses – Connectors – Light Sources – Light Detector – Systems Components – Advantages and Disadvantages.

Unit:5	FIBER OPTICS SENSORS AND	18								
	APPLICATIONS	Hours								
Intensity	Modulated Sensors - Hybrid sensors - phase sensors - Diffraction Grating Sensors	nsor								
	ometric sensor - Fiber Fabry Perot Fiber Optic sensor - Chemical Senor Comi									
Antenna	Television – Networking - Digital Video Transmission.									
	Total Lecture hours 90									
		Hours								
Text Boo	ok(s)									
	Kennedy; Davis, Electronic Communication Systems", Tata McGraw Hill Publishing Company Limited, III edition									
2	Gerd Keiser, —Optical Fiber Communication", McGraw Hill Publication, I	V^{th}								
	Edition, 2011.									
Referenc	ee Books									
1	Samuel Y. Liao , "Microwave Devices and Circuits", PHI, III Edition.									
	Govind P. Agrawal, Fiber Optic Communication Systems",									
2	WileyPub <mark>lications, IVth Edi</mark> tion, 2012.									
	Online Contents [MOOC, SWAYAM, NPEL, Website etc.]									
	https://nptel.ac.in/courses/108/103/108103141/Introduction to microwave									
	Engin <mark>eering </mark>									
2	https://www.classcentral.com/course/swayam-microwave-engineering-	1								
	14199 Microwave engineering swayam course	4 .								
	Course Designed By:									
D	r.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tiru	-								
	Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode	. / /								

VIII O										
Mapping with Programme Outcomes										
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
		00						6		
CO1	S	S	M	M	M	M	S	M	M	S
			6				3.8			
CO2	S	M	M	5 M	M	₂ M	S	L	L	L
				Duca	TE TO EL	EVATE				
CO3	S	M	S	M	M	L	L	L	M	M
				200	ATE TO	ELEVAN				
CO4	S	S	M	S	S	S	L	L	M	M
CO5	S	S	S	S	M	L	M	M	S	M

^{*}S-Strong; M-Medium; L-Low

Course code	5EG	AUTOMOTIVE ELECTRONICS	L	T	P	C
Core/Ele	ctive/	Elective-II –G	1			_
Suppor	tive		4	T		3
Pre-req	uisite	Basic Electronics	Sylla Vers		202	5-26
Course Objec	tives:		1	I		
The main object	ctives of th	is course are to:				
		e concepts of Automotive Electronics and its evolution	and			
		ve systems &subsystems overview.				
		nsors and sensor monitoring mechanisms aligned to				
	-	ems, different signal conditioning techniques, interfacir	ıg			
	ques and a	esign and model various automotive control systems				
		levelopment technique.				
using ivid	der based (de veropinent teeninque.				
Expected Cou	rse Outco	mes:				
_		etion of the course, student will be able to:				
	an <mark>overvi</mark>				K	[2
2 Interface a	a <mark>uto</mark> moti <mark>ve</mark>	sensors and actuators with microcontrollers			K	[4
	d the designation	n cycles, communication protocols and safety systems e industry.	emplo	yed	K	(2
		ne management systems			K	2
5 Analyze E	Engine Man	nagement System			K	[4
K1 - Rememb	per; K2 - U	nderstand; K3 - Apply; K4 - Analyze; K5 - Evaluate;	K6 – (Create	e	
Unit:1		INTRODUCTION		18	hou	rs
Wiring System	ns, Circui	Operation, Electrical Wiring Terminals and Switt Diagrams and Symbols. Charging Systems and bles, Alternations and Charging Circuits, Basic Starting	Start	ing S		
Unit:2		IGNITION SYSTEMS		18	Shou	rs
Distribution L	ess Ignition	ectronic Ignition Systems. on, Direct Ignition, Spark Plugs. Electronic Fuel elling and Exhaust Emissions, Electronic Control of el Injection				
Unit:3		INSTRUMENTATION SYSTEMS		18	houi	S
	entation Sy	ntation Systems, Various Sensors Used for Different F ystems, Vehicle Condition Monitoring Trip Computer,				
Unit:4		FRONIC CONTROL OF BRAKING AND		18	hou	rs
<u></u>		TRACTION				-
Introduction at	nd Descrip	tion Control Elements and Control Methodology, Ele	ctronic	c Cor	ıtrol	of
	-	. Introduction and Description Control Of Control				

Automatic Transmission: Introduction and Description Control Of Gear Shift and Torque

Converter Lockup, Electric Power Steering, Electronic Clutch

Unit:5	ENGINE MANAGEMENT SYSTEMS	18hours
Combined Igr	ition And Fuel Management Systems, Exhaust Emission Cont	rol, Digital Control
Techniques, C	omplete Vehicle Control Systems, Artificial Intelligence and E	ngine Management,
Automotive	Microprocessor Uses. Lighting a	and Security
Systems:Signa	${\tt lllingCircuit, Central Locking} and {\tt Electric Windows Security System}$	ıs,
Airbags and S	eat Belt Tensioners.	
	Total Lecture hours	90hours
Text Book(s		
1 TOM DE	NTON, Automobile Electrical and Electronic Systems, Edwar	d Arnold pb., 1995
Reference B	ooks	
1 1.DON	KNOWLES, Automo <mark>tive Electronic</mark> and Computer controlled	Ignition
Systems	, Don	
2 WILLIAI	M, T.M <mark>., Automotive Mechanics, McGraw Hill Book</mark> Co.,	
3 WILLIAI	M, T <mark>.M., Automotive Electronic Systems, H<mark>e</mark>iemann Ltd., Lond</mark>	don, 1978.
4 Ronald K	Jurg <mark>en, Automotive Electronics Handboo</mark> k, McGraw Hill, Inc	, 1999.
Related Onl	ine Contents [MOOC, SWAYAM, NPTEL, Websites etc.]	
1 https://np	tel.ac.in/courses/107/103/107103084/	
2 https://np	tel.ac.in/courses/107/106/107106088/	
3 https://ww	vw.youtube.com/watch?v=vJ4EfyGXehg	
4 https://ww	vw.youtube.com/watch?v=BG4N2dBgJrQ	
N N	Course Designed By:	

Dr.K. Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappir	ng with Pro	gram <mark>me</mark>	Outcon	nes	TIM			57		
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	Seg	M	M	M	S	L	L	S
CO2	S	S	SED	CATE	M	M	S	L	L	L
CO3	S	S	S	M	M	L	L	S	S	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	S	L	M	M	S	S

^{*}S-Strong; M-Medium; L-Low

Course Code	5EH	SATELLITE COMMUNICATIONS	L	T	P	C
Core/Elective/S	Supportive:	ELECTIVE-II-H	4	T		3
Pre-requ	isite•	PRINCIPLES OF	Syll	abus	202	5-26
		COMMUNICATION	ver	sion		
Course Objective						
The objectives of						
		on fundamentals of Advanced Computer des				
		ept of instruction level parallelism, pipelining	ng and	memor	y	
-	associated wit					
• To ennance	e the knowled	lge on advanced processors.				
Expected Course	Outcomos	- NOBEDA :				
_		e course, student will be able to:				
		lvanced computer design principles.				K1
		el computer model with instruction level pa	rallelis	m		K4
	owle <mark>dge on pi</mark>	1	ituitoiis	111.		K2
		ierarchy in developing an advanced compu	ter.			K2
		concepts in advanced processors.				K3
117		erstand; K3 – Apply; K4 – Analyze; K5 –	- Evalu	ate: K	6 – Cre	
Unit: 1		ATELLITE SYSTEMS – OVERVIEW				Iours
Unit: 2 Orbital Mechanic and launch vehicl Unit: 3 Introduction- space and command- po	orbital effectors orbital effe	cations of satellite communications over othe TAL ASPECTS OF SATELLITE SYSTE determination- orbit perturbations- Orbital dects in communication systems performance THE SPACE SEGMENT tems- attitude and orbit control systems- Tecommunication subsystems.	determi e.	nation-	18 H launch 18 H ing	Iours
Unit: 4	, S.	SATELLITE LINK DESIGN	C 1	1' 1		Iours
		em noise temperature and G/T ratio- Design ink for specified C/N.	1 OI GOV	vn iink	s- up	
Unit: 5		LICATIONS OF SATELLITE SYSTEM	IS		18 H	Iours
		SAT- GSM- GPS- INMARSAT-Direct Broad		satellite		
	roadcast (DTF	H)- Digital audio broadcast (DAB)- World s			`	,
TV(DTV) GRUII	VISTIT:	Total Lectur	а Наш	•6	00 T	Iours
		Total Lectur	C 110ui		70 1	louis
Text Books						
1 Timothy Pra willey, 2006		ostian,Jeremy Allnutt, Satellite Communica	tions, 2	nd editi	on, Joh	n
2 W. L. Pritch Engineering	nard, H. G. Sug g, 2 nd edition, I	yderhoud and R. A. Nelson, Satellite Comm Pearson educational publishers, New Delhi,	nunicati 2003.	ion sys	tems	
Reference Books						
		Communications, 3 rd edition, Mc Graw Hill,	Interna	tional.	2001.	

2

Dr D.C. Agrwal, Satellite Communications, 4thedition, Khanna Publications, NewDelhi, 2001.

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

https://nptel.ac.in/courses/117/105/117105131/

https://www.youtube.com/watch?v=hXa3bTcIGPU

https://www.youtube.com/watch?v=BvjlBpP4zU8

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

		Mapping with Program Outcomes													
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10					
CO1	S	L	L	L	L	- L	L	L	L	L					
CO2	S	M	L	M	M	L	L	L	L	M					
CO3	S	M	M	L	L	L	L	L	L	L					
CO4	S	S	M	M	M	M	L	L	L	M					
CO5	S	L	L	L	L	L	L	L	L	M					

*S-Strong; M-Medium; L-Low

Colmbalare

பாரை உயர்த்திட

Course code	6EI	INDUSTRIAL AND POWER ELECTRONICS	L	T	P	C		
Core/Elect Supporti		Elective-III –I	6	Т		3		
Pre-requis	site	Basic knowledge of Electronic Circuits or	Sylla	abus	202	5-26		
1		permission of instructor		sion				
Course Objective	es:		ı					
The main objective	es of this	course are to:						
❖ To presents	the princi	ples and applications of industrial and power electro	nics					
To enable the	ne student	s to learn and design industrial and power electronic	circu	uits				
		es designing skills related to the power electronics are ial electronics	nd un	dersto	ood			
Expected Course								
On the successful	l com <mark>pleti</mark>	on of the course, student will be able to:						
1 Developed the	e Circuit o	lesigning skills power electronics. Understood the			K	1		
concept indus	tri <mark>al elect</mark>	ronics system design.						
2 Acquire know electronics.	2 Acquire knowledge about fundamental concepts and techniques used in power							
4 Foster ability to identify basic requirements for power electronics based design application.								
5 To develop sk	<mark>cills to bu</mark> i	ld, and troubleshoot power electronics circuits.			K:	5		
K1 - Remember;	K2 - Und	lerstand; K3 - Apply; K4 - Analyze; K5 - Evaluate;	K6 -	- Crea	te			
Unit:1	100	THYRISTORS		n /	15h	ours		
Introduction—Pr Model — GTO — SC		Construction, Operation and Characteristics of SCR - SBS - LASCR.	– Tw	o Tra	nsisto	r		
Unit:2	0000	TURN ON/OFF MECHANISMS	S		15 h	ours		
Thermal Triggering Off Methods: Natu	g– Radiat ural Comi	rn on Methods: AC Gate Triggering: Forward ion Triggering– DC Gate Triggering: Pulse Trigger mutation – Forced Commutation: Self Commutation attains	ing -	- Тур	es of T	Turn		
Unit:3		CONTROLLED RECTIFIERS & INVE	RTE	RS	15 h	ours		
Inductive Load –S	Single pha	Half Wave Controlled Rectifiers with Resistive Inse Full Wave Controlled Rectifiers with Resistive of Voltage Inverters						
Unit:4		CYCLO CONVERTERS AND CHOPI	PERS	S	15ho	ours		
		Centre Tapped Step-Up Cyclo Converter – Single Place – Three Phase to Single Phase Cyclo converter – S						
Unit:5		APPLICATIONS			15 h	ours		
Introduction –Diele	ectric Hea	ting – Induction Heating – SMPS – UPS – Static Ci	rcuit	Break				
		Lighting System – Time Delay Control – Static Sw						
		Total Lecture ho	urs		75 ho	ours		

	-		/ \
Text	Boo)K	(S)

- 1 MDSingh,—**Power Electronics**, 2ndEdition, Tata-McGrawHill, 2007.
- 2 M.Ramamoorthy, —**ThyristorandtheirApplications**, 2ndEdition, EastWestPvt.Ltd, 1999

Reference Books

- 1 Harish C Rai, "Industrial and Power Electronics" 10th edition, Umesh publications 2002
- 2 Timothy J Maloni, "Industrial Solid State Electronic Devices and Circuits" 2nd edition 1986

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

- 1 https://www.youtube.com/watch?v=1Auay7ja2oY
- 2 https://www.youtube.com/watch?v=oqnLQVFaqYI
- 3 https://www.youtube.com/watch?v=naxnRkOfh2Q

Course Designed By:

Dr.K. Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	ng with <mark>P</mark> a	<mark>rogram</mark> n	ne Outo	comes						
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
	4		100	FERT	1			9		
CO1	S	S	S	M	M	M	S	L	L	S
			1000	1-600	2000	7 '	- /	*		
CO2	S	S	S	M	M	M	S	L	L	L
)	The	5				
CO3	S	S	S	M	M	L	L	S	S	M
	ે જ				N. B.					
CO4	M	M	M	S	S	S	L	L	M	M
		000		1						
CO5	M	M	S	S	S	L	M	M	S	S
	13		3				- 81			

*S-Strong; M-Medium; L-Low Spaint 2 LINES FOUCATE TO ELEVATE

Course Code	6EJ	ROBO	OTICS	L	T	P	C
Core/Elective/		Electiv	e III –J	6	Т		3
Supportive:							
Pre-Requisite:		Basic Electro	nics	Syll	abus	2025	5-26
				Vei	sion		
Course Objective	es:						
The Main Object	tives of thi	s course are to:					
 To familia 	rize the stu	dents with the application	ations of Robots				
To know a	bout the se	ensors, <mark>actuators used</mark>	in Robots designi	ng			
Expected Course	Outcome	s:					
-		n of the course, stude	nt will be able to:				
1 Describe t	he working	g concept and types of	f Robots	0	ŀ	Κ3	
2 Apply the	knowledge	e of types of sensors a	and actuators		ŀ	ζ2	
3 Programm	ning Langu	ages for Robot design	n models		ŀ	ζ6	
4 Understan	d the conc	ept of Mobile Robotic	C Locomotion		ŀ	Κ2	
5 Study the	<mark>variou</mark> s app	olications of Robots	3 1		ŀ	ζ3	
K1:Remember;	K2-Und	erstand; K3 -Apply;	K4-Analyze; K5	- <mark>Eval</mark> ua	te;	K6 -C1	reate
Unit:1	3 00	FUNDAMENTALS	OF ROBOTS			15 hou	urs
Introduction: Aut	tomations a	and Robotics - Roboti	cs Drive System -	Robot A	Anator	ny - W	/ork
		nd Precision of Move					
		gnition Sub System:					
- Actuation Syste	ems - Conti	ol Method - Program	Method.				
Unit:2		ROBOT END E		AS.		15hou	rs
		Ef <mark>fectors - Mec</mark> hanica		ART A SECOND	_	=	
_ \/\	A Wall	<mark>Gripper - Adhesive</mark> G		17 No. 18 7	her Mi	scella	neous
	s End Effec	ctors - C <mark>onsiderations</mark>	in Gripper Selecti	ons and			
Design Unit:3	30.0	SENSORS IN R	OPOTC			15 hoi	
	tion Into	ernal Sensors - Exter		on Creat			
		- Proximity and Ran		-			
		es of Sensors in Rob					
-		ators - Electric Actuat					
Unit:4		ILE ROBOTICS LO				15 ho	urs
Introduction Key	Uses for I	ocomotion - Leg Mo	bile Robots - Leg	Configu	ration	and	
Assembly - Cons	ideration f	or Dynamics - Types	of Legged Robot I	Locomo	tion - (One Le	eg;
Two Legs Biped	- Four Leg	s (Quadruped) - Six I	Legs (Hexapod) - 0	Concept	of Wh	eeled	
Mobile Robots							
Unit:5		ROBOTIC APPLIC				15 hou	
		aterial Handling - P					_
• • •		spection Application			_		nbly
	•	- Non Industrial App				or -	
neam Sector - S	ervice Sect	tor - Agriculture Farm				L	
			Total Lectu	re nour	s 75	hours	Š

Text 1	Book(s)
	M.P.Groover, Mitchellweiss, Roger. N. Nagel, Nicholas G. Odrey, Industrial
1	Robotics— Technology, programming and Application , McGraw-
	Hill, 2008.
2	Ghosh,—Control in Robotics and Automation: Sensor Based Integration", Allied
	Publishers, Chennai, 1998.
Refer	ence Books
1	Deb. S.R., —Robotics Technology and flexible Automation , John Wiley, USA
	1992.
2	KlafterR.D., ChimielewskiT.A., Negin M., —Robotic Engineering—An integrated
	approach, Prentice Hall of India, New Delhi, 1994.
Relat	ed Online Contents [MOOC <mark>, SWAYAM, NPEL</mark> , Website etc.]
1	https://nptel.ac.in/courses/112/105/112105249/Introduction to Robots
2	https://nptel.ac.in/courses/112/101/112101098/Robotics and Automation
	Course Designed By:
	Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur
	& Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode
	& Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode

Mappi	ng with <mark>P</mark> i	<mark>rogram</mark> n	ne Outo	comes			2	(C)1 -		100
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
			-	4		A. C.		1		
CO1	S	S	M	M	M	M	S	M	M	S
			1		5)					
CO2	S	M	M	M	M	M	S	L	L	L
					7				9	/-
CO3	S	M	S	M	M	L	L	L	M	M
		(. 14-6	Man V				.5		
CO4	S	S	M	S	S	S	L	L	M	M
					Ceimbate	re /		9	8	
CO5	S	S	S	S	M	L	M	M	S	M
			~ B	Fig.		TILL -	57			

^{*}S-Strong; M-Medium; L-Low

Course code	6EK	PROGRAMMABLE LOGIC	L	T	P	C
		CONTROLLERS				<u></u>
Core/Elective/ Supportive:		Elective III – K	3	Т		6
Pre-req	uisite	Digital Electronics and computer Architecture and Organization	Syllal Versi		2025	-26
Course Object	tives:					
		his course are to:				
output m To train to description Apply PI	odules. the stude on and un LC Time	nts to create ladder diagrams from process control nderstand various types of PLC registers and Counters for the control of industrial processes, a Handling Functions.		it and	1	
Expected Cour						
On the success	sful com	pletion of the course, student will be able to:				
understand omodules	di <mark>ffer</mark> ent	Programmable Logic Controllers and will types of Devices to which PLC input and output			K	
control desc	riptions	ut various types of PLC registers, ladder diagrams from	n proce	ess	K2	
3 Develop a co	oil and co	ontact control system and analog PLC operations			K4	1
4 Apply time	delay on	PLC operations	9 /		K3	3
		types PLC functions, data handling functions and its v		Стоо	K2	2
	Del, K 2 -	Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate	e, Ku -			
Unit:1	, Q	PROGRAMMABLE LOGIC			hour	<u> </u>
Arrays (PLAS) Programmable	, Progran Gate Arr (PLDs) -	Introduction - Programmable Logic Structures - Programmable Array Logic (Pals), Programmable Gate Array rays(FPGAS) - Sequential Network Design With Program of Sequential Networks Using ROMs and Flast Using PAL PROGRAMMABLE LOGIC	rs (PGA rogramr	.S), F nable	Field	
		CONTROLLERS				
Programmable Operation-PLC	•	ontrollers(PLCS) - Introduction Parts Of PLC - Pro- - PLC Hardware Components - I/O Section- Analog	-			
Unit:3		BASICS OF PLC PROGRAMMING		15	hours	;
OFF Instruction Switches – Mec	ons -Ele hanically s - PLC l	ple Instructions - Programming EXAMINE ON octromagnetic Control Relays-Motor Starters - May Operated and Proximity Switches - Output Con Ladder Diagram - Converting Simple Relay Ladderam	nually trol D	Ope Devic	rated es -	
Unit:4	1	PLC INSTRUCTIONS		15	hour	<u> </u>
Оши.4		I LC INSTRUCTIONS		13	nour	<u> </u>

Unit:5	APPLICATIONS OF PLC	15hours							
Applicat	Applications of PLC - Simple Materials Handling Applications - Automatic Control of								
Warehou	Warehouse Door - Automatic Lubricating Oil Supplier Conveyor Belt - Motor Control								
Automa	Automatic Car Washing Machine - Bottle Label Detection - Process Control Application								
	Total Lecture hou	rs 75 Hours							
Text B	Text Book(s)								
1	Charles H. Roth, Jr "Fundamentals of Logic Design", Fourth Edition, Jaico Publishing								
2	Frank D. Petruzella'' Programmable Logic Controllers '', McGraw- Hill book, company,								
3	3 Siemens "PLC Handbook ".								
Refere	Reference Books								
1	1. William I. Fletcher "An Engineering Approach to Digital Design	n '', Prentice, Hall							
	of India Ltd., New Delhi, 1999.								
Relate	Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]								
1	1 https://unitronicsplc.com/what-is-plc-programmable-logic-controller/								
Course Designed By:									
	Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur &								
	Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.								

Mapping with Programme Outcomes										
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	S	M	M	S
CO2	S	M	M	M	M	M	S	©'L	L	L
CO3	S	M	S	M	M LIM	LUI	L	L	M	M
CO4	S	S	M	S	IE SO E	S	L	L	M	M
CO5	S	S	S	S	M	L	M	M	S	M

^{*}S-Strong; M-Medium; L-Low

Course Code 6EL	VLSI DESIGN	VLSI DESIGN L					
Core/Elective/Supportive:	ELECTIVE – III-L	6	T		3		
Pre-requisite:	Digital Principles and	abus		2025-26			
r re-requisite.	Applications						
Course Objectives:							
The objectives of this course a	re:						
1	on Fabrication Process of NMOS	PMOS ,	CMO	S AND)		
BICMOS, Super integr							
	analyze the electrical properties o				ign		
•	out diagrams for MOS transistors,				4: 4:_		
	et of floor planning, placement, ro		ı pow	er dela	y estimatio		
	igital circuits and memory design. f Com <mark>binational and</mark> Sequential C		etina				
11.7	Compinational and Sequential C	Trout To	sung.				
Expected Course Outcomes:	. 1						
	ne course, student will be able to:				IZ 1		
	n fabrication principles.	40.00			K1		
	ectrical properties of MOS transis		for o		K4		
design.	layout design rule to create a VL	1137			K6		
4 Understand the physical design steps and gain the knowledge on types of VLSI design styles. K2							
Gain the knowledge, a	analyze and apply test principles to	o e <mark>valuat</mark>	e the		K5		
VLSI designs.	rstand; K3 – Apply; K4 – Analy	WE WE	Evol	lunctor 1	V6 Cross		
Unit: 1	VLSI TECHNOLOGY	ze; K5 -	- Eval	luate; 1	15 Hou		
	s flow – Testing – Super integration	on conco	nta I	ntagra			
	and capacitors – Crossovers – NN						
BICMOS fabrication processes		105 11	VIOS	CIVIO			
FLE	CTRICAL PROPERTIES OF N	IOS A	45	7	7		
Unit: 2	DEVICES	200	5		15 Hou		
	ersus Drain to source voltage (Vo						
3,7	t) – MOS transistor trans-conduct		and oi	itput co	onductance		
Unit: 3	s transistor- pull – up to pull – dov	vii tauo.			15 II		
	DESIGN PROCESSES agram design rules with examples	, г	Josian	rules f	for Layo		
ĕ			_		•		
	neet resistance Rs—standard unit of MOS circuits – limitations of sca	-	ınce–ı	nverter	delays-		
	PHYSICAL DESIGN AND STY				15 Hou		
PHYSICAL DESIGN:	I II I SICAL DESIGN AND SI	פתוח			13 1100.		
	Routing – Power Delay Estimatio	n – Cloc	k Rou	tino — ¹	Power		
Routing.	Rodding Tower Delay Estillatio	11 C10C	k KUU	ung – I	OWLI		
VLSI DESIGN STYLES:							

TESTING OF VLSI CIRCUITS

Test Principles-BIST-Test Bench- Combinational Circuit Testing, Sequential Circuit Testing, Test Bench Techniques.

Total Lecture Hours

15 Hours

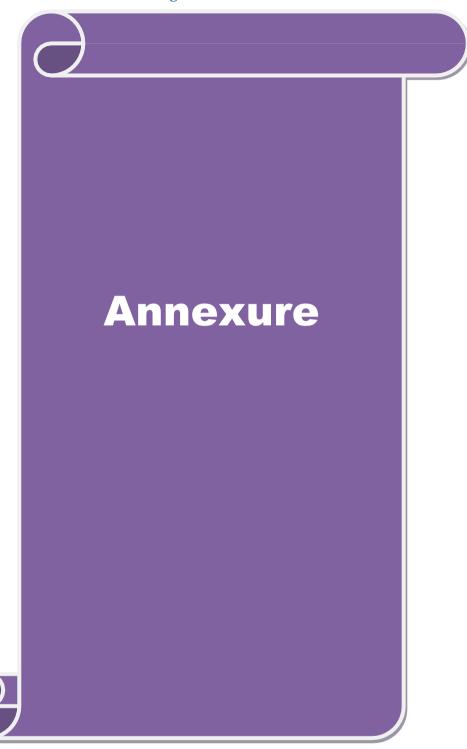
75 Hours

Unit: 5

Text B	Text Books						
1	Basic VLSI Design, Douglas ,3rd Edition, A. Pucknell, Kamran Eshraghian, PHI, New						
	Delhi, 2011.						
2	Modern VLSI design, Wayne Wolf, 3rdEdition, Pearson Education, New Delhi, 4th						
2	impression 2008.						
Refere	nce Books						
1	Introduction to VLSI Circuits and Systems, John .P. Uyemura, John Wiley, Student						
1	Edition, New Delhi, Reprint 2006.						
2	Principles of CMOS VLSI Design, N.H.E Weste, K.Eshraghian, Adisson Wesley, 2nd						
	Edition, New Delhi.						
3	Application Specific Integrated Circuits, Michel John Sebastian Smith, Addison Wesley,						
3	Indian Edition, 4th Indian Reprint 2001, New Delhi.						
Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]							
1	https://nptel.ac.in/courses/117/101/117101058/						
2	https://www.youtube.com/watch?v=9SnR3M3CIm4						
3	https://www.youtube.com/watch?v=Y8FvvzcocT4						
Course Designed By:							
	Dr.K.Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur &						
Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.							

	Mapping with Program Outcomes									
Cos	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	S	L	L	L	L	L	L	CL /	L	L
CO2	S	S	L	S	M	L	M	L	L	L
CO3	S	M	S	L _{Coin}	S	L	M	M	L	S
CO4	S	L	M	L	S	ILITED BY	L	L	M	L
CO5	S	S	M	≈ M-III DUCATE	FLEVA	TE L	L	L	M	S

^{*}S-Strong; M-Medium; L-Low



BHARATHIAR UNIVERSITY: COIMBATORE 6410 46. DEPARTMENT OF DEPARTMENT OF ELECTRONICS

MISSION

- To develop appropriate facilities for promoting research activities
- To inculcate leadership qualities among students for self and societal growth
- To nurture students on emerging technologies for serving industry needs through industry institute interface
- To enrich teaching learning process by transforming young minds to be resourceful engineers

