

BHARATHIAR UNIVERSITY, COIMBATORE.
M. Sc. APPLIED ELECTRONICS DEGREE COURSE WITH COMPULSORY
DIPLOMA IN INFORMATION TECHNOLOGY
(AFFILIATED COLLEGES - Effective from the academic Year 2008-2009)
SCHEME OF EXAMINATIONS – CBCS PATTERN

Sem.	Study Components	Course title	Ins. hrs/ week	Examinations				Credit
				Dur.	CIA	Marks	Total Marks	
I	Paper I - Microwave and Radar Navigation Systems		5	3	25	75	100	5
	Paper II - Microcontroller and Applications		5	3	25	75	100	5
	Paper III - Micro Electro Mechanical Systems and Power Electronics		5	3	25	75	100	5
	Paper IV - Design of Analog Integrated Circuits		4	3	25	75	100	5
	Practical I - General Electronics Lab		4	-	-	-	-	-
	Practical II - Embedded & VHDL Lab		4	-	-	-	-	-
	Dip.Paper I : Web Technologies		3	3	25	75	100	3
II	Paper V - Embedded Systems		5	3	25	75	100	5
	Paper VI - VHDL Programming		5	3	25	75	100	5
	Paper VII - DSP Processor And Architecture and Programming		5	3	25	75	100	5
	Paper VIII-High Performance Communication Networks		4	3	25	75	100	5
	Practical I - General Electronics Lab		4	4	40	60	100	4
	Practical II - Embedded & Vhdl Lab		4	4	40	60	100	4
	Dip. Paper II: Relational Data Base Management Systems		3	3	25	75	100	3
III	Paper IX - Advanced Digital Signal Processing		5	3	25	75	100	5
	Paper X - PC Based System Design		5	3	25	75	100	5
	Paper XI - Wireless Communication Engineering		5	3	25	75	100	5
	Paper XII - Elective Paper		4	3	25	75	100	5
	Practical III - PC Hardware Lab		4	4	40	60	100	2
	Practical IV - DSP Lab		4	4	40	60	100	2
	Dip. Paper III : Programming with .Net		3	3	25	75	100	3
IV	PROJECT WORK & VIVA VOCE		10	-	-	-	200*	6
	Dip. Practical: RDBMS Laboratory and .NET Laboratory		3	4	40	60	100	3
	Total						2200	90

* Project report - 160 marks; Viva-voce – 40 marks

Elective Paper

1. Verilog and Asic Design
2. Bio-Medical Instrumentation
3. Computer Architecture and Parallel Processing

Note: The syllabi for the above papers be the same as prescribed for the academic year 2007-08